

# INSTRUCTION MANNUAL SECTION

We are confident that you will be entirely satisfied with your 430MHz Transceiver Model C78. Our very strict quality control and inspection ensure that each transceiver unit leaves the factory in perfect condition. If the unit is damaged or fails to operate properly, immediately contact your dealer.

To obtain the best performance and longest use from your transceiver, study these instructions carefully.

## 1. PRECAUTIONS

# ANTENNA

- When you wish to use your C78 Transceiver as a portable operation, attach the supplied antenna to the BNC antenna jack on the front panel of the unit.
- For mobile or base-station operation, disconnect the supplied antenna from its front jack and attach an appropriate external antenna to the jack on the rear of the unit. If an external antenna is used with the supplied antenna attached to the unit, in parallel unsatisfactory communication may result.

## POWER SUPPLY

When inserting battery packs in the unit, check their polarity.

The required number of battery cells depends on battery type: ten (10) rechargeable Ni-Cad battery cells (1.2V each) or nine (9) UM-3 manganese or alkaline battery cells. External power sources are acceptable for mobile or base-station operation. For more details, see items (6) and (7) in paragraph 4.

## INSTALLATION NOTES

- Install your transceiver in a dry, dust-free and well-ventialted place. The unit should not be subjected to extremely high temperatures or humidity. It must not, under any circumstances, be exposed to direct sunlight.
- 2. Provide adequate space behind and under the unit for free circulation of air.
- In a mobile installation, exercise special care to allow enough space behind the unit for adequate heat dissipation from the heat sink. Take measures to ensure that the unit is not subjected to excessive vibrations or shocks during operation.

# 2. FEATURES

The C78 Transceiver incorporates sophisticated microcomputer-assistance. The built-in microcomputer memorizes, computes and makes decisions for quick and precise channel frequency control.

# The microcomputer provides the following useful features:

- Memory capacity up to 5 channel frequencies can be stored arbitrarily.
- 2. Automatically scans up to five stored channel frequencies.
- The internal offset memory holds a frequency shift span. This makes the C78 for repeater operation.
- 4. Divides the frequency band of 430  $\sim$  439 MHz into 10 sub-bands of 1 MHz bandwidth, and scans each 1MHz sub-band at 25kHz or 50kHz intervals.
- Automatically searches either for busy or vacant channels.
- Automatically switches frequency scan speed in two steps.
- Allows channel scan stepping of 25kHz and 50kHz spans.
- The MHz button achieves quick frequency advancement in 1MHz steps.

# DIGITAL FREQUENCY READOUT USING LIQUID CRYSTAL DISPLAY (LCD)

The C78 employs a 4-digit frequency readout using LCD. The LCD has low power-consumption, is easy to read even in direct sunlight and thus makes the C78 suitable for outdoor operation.

At night, it is lit by LCD illumination. The digital readout panel also carries memory address and scan indicators and battery saver ON/OFF markings for simple operation checks. checks.

When the set is tuned to an off-band during cross operation, the frequency readout provides an OFF display instead of a frequency display.

#### FUNCTIONAL BENEFITS

BATTERY SAVER

The battery saver senses the presence of receiver input signals and automatically reduces the power supply to the unit when it is set up in the reception standby mode. It thus saver battery power by half and can be activated by simple front key operation.

#### 400 CHANNELS SELECTABLE

Up to 400 channels can be selected using the noncontact channel selector which has 18 steps per rotation (200 channels at 50 kHz interval and 400 channel at 25 kHz interval).

 MICROPHONE WITH A FREQUENCY UP-DOWN CONTROL:

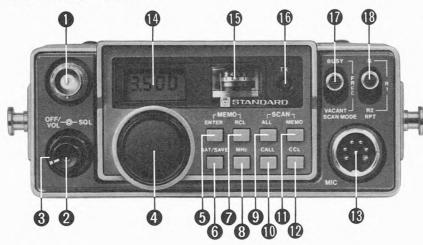
The attached hand microphone is a built-in frequency up-down control for easy and continuous chanel selection.

## OTHER FEATURES

- A single VCO is shared by both the receiver and transmitter.
- APC circuit protects the final transistors against supply voltage variations.
- Audible check tone provided for key and up-down control operation.
- Built-in tone burst generator for 1750 Hz for repeater driving.

# 3. PANEL FEATURES

## FRONT PANEL FEATURES



# BNC CONNECTOR

This connector accepts the supplied antenna for portable operation.

## POWER/VOLUME CONTROL

This knob serves as both the POWER switch and VOLUME control. Clockwise rotation turns the unit on and then increases receiver output volume.

# SQL (SQUELCH) CONTROL

The SQL control is used to suppress annoying FM background noise heard when no input signal is present. Set this control at a point where background noise just disappears.

# CHANNEL SELECTOR

This selects channel frequencies in 25 or 50kHz steps. To increase channel frequency turn it clockwise.

# MEMO ENTRY KEY

Press this key to store the selected channel frequency in the memory. The memory can store up to 6 frequency data, the 6th memory address being assigned to an offset memory for repetitive operation.

The offset memory is used to shift channel frequencies for cross operation, by the stored frequency span.

## 6 BAT/SAVE

This key, when pressed, activates the internal battery saver to saver battery consumption in the receiver standby mode. While this key is depressed, a small dot appears at the right margin of the frequency readout, indicating that the saver is activated.

The battery saver remains inactive when the squelch is off or during scan operation. When activated, power consumption in the reception standby mode is reduced by approximately half.

## MEMO RCL KEY

This key is used to recall stored channel frequencies. Each time it is pressed, frequency data stored in memory addresses M1  $\sim$  M5 and in the offset memory address are recalled sequentially. The recalled memory address is displayed at the bottom of the frequency readout along with memory mark "M".

When the offset memory content is recalled, a small dot appears at the top left of the frequency readout along with memory mark "M" displayed at the bottom of the readout.

# MHz KEY

This key selects the mega-hertz order of channel frequencies from 340 to 439MHz in 1MHz steps. Each time this key is pressed, channel frequency is incremented 1MHz. When it is pressed and held, channel frequency automatically increases in 1MHz steps.

# SCAN ALL KEY

This key, when pressed, scans up over the currently-selected MHz band in 25 or 50kHz steps.

# CALL

Press this key to transmit a tone burst signal for repeater driving (tone frequency: 1750Hz).

# SCAN MEMO KEY

When this key is pressed, the channel frequencies stored in memory addresses  $M1 \sim M5$  are scanned sequentially (the frequency stored in the offset memory is not scanned). Memory addresses in which no frequency data is present are automatically skipped.

## CCL KEY

This key is used to initialize all the unit's operation mode of the unit.

## B MIC JACK

This jack accepts the supplied microphone.

# FREQUENCY READOUT AND MODE INDICATORS

This LCD display carries a frequency readout, SCAN, MEMO, BATT/SAVER, and offset memory indicators.

When a frequency of, for example, 433.500 MHz is received, the frequency readout displays the four low-order digits of the frequency as "3.500". In the SCAN mode, indicator "S" is displayed in the marginal area of the display. In the MEMORY mode, indicator "M" and a pertinent memory address of 1 to 5 are displayed at the bottom of the display. When an empty memory address is accessed, indicator "M" blinks. When the offset memory is accessed, a small dot appears at the top left of the display.

# METER

The meter checks input signal strength (S), transmission power (RF) and battery voltage. The 'meter is switched automatically between S and RF as the unit is switched from the reception to transmission mode. When you wish to check battery voltage with the meter, set the rear slide switch (19) to the BAT CHECK position.

## TX INDICATOR LAMP

This goes on when the unit is set up for the transmission mode.

# SCAN MODE SWITCH

The BUSY position of this switch searches for busy channels during channel frequency scanning. The VACANT position searches for vacant channels. In both the BUSY and VACANT positions, channel scanning takes place at high speed (0.25 sec/step). In the FREE position, channel scanning takes place at low speed (2.0 sec/step).

# RPT SWITCH

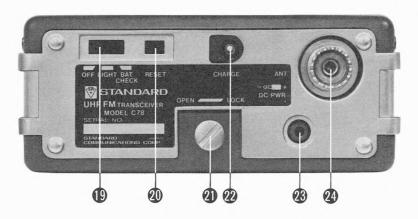
This switch selects between the simplex and repeater (R1 and R2) modes.

- Position S: For normal simple operation.
- Position R1: To shift the transmission frequency upwards by the fre
  - quency span stored in the offset memory.
- Position R2: To shift the reception frequency upwards by the frequency span stored in the offset memory.

## NOTE:

In the R1 and R2 modes, the memory write, recall, and scan modes are inhibited.

#### REAR PANEL FEATURES



## LIGHT/BAT CHECK SWITCH

In the LIGHT position, the LCD and meter lamps glow. The BAT CHECK position causes the front meter to function as a battery voltage indicator while all the lamps are left on. The OFF position turns off all lamps.

## RESET BUTTON

This switch is used to reset the internal microcomputer to its initial state in the case of a malfunction.

## BATTERY COMPARTMENT COVER RETENTION SCREW

To open the battery compartment cover, loosen this screw with a coin.

## CHARGE SOCKET

This socket accepts a Ni-Cad battery charger (see Fig. 3).

# SIDE PANEL

## B EXT SPK JACK

This jack accepts an external speaker or earphone with an impedance of  $8\Omega$ .

## STRAPPING POST

Attach the supplied shoulder belt to this post. See Fig. 5.

# EXT. PWR SOCKET

This socket accepts an external DC powersource (13.8VDC) supplied via the DC adaptor (which is supplied with the unit). See Fig. 3.

#### ANT RECEPTALCE

This M-type socket accepts an external antenna with an impedance of  $50\Omega$ . When an external antenna is to be used, disconnect the supplied antenna from its front socket.

## INTERNAL SWITCHES

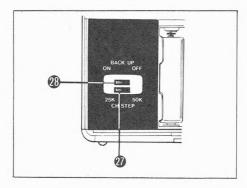
# CH STEP SWITCH

This switch selects channel frequency scan stepping between 25 and 50kHz. It is located behind the battery compartment cover.

# BACK UP SWITCH

When this switch is on, frequency data stored in the internal memory remains intact even when the power switch on the unit is turned off.

If the unit is to be out of use for any length of time, ensure that this switch is off.



## MICROPHONE

## UP-DOWN CHANNEL CONTROL BUT-TON

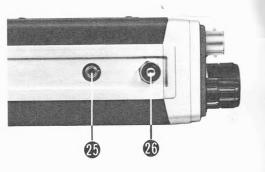
If the button is held down, channel frequency is tepped up or down continuously.

## **()** PTT BUTTON

cutively.

To put the transceiver into the transmission mode, push the PTT button. To transmit a tone-burst signal for repeater driving, press this button twice conse-





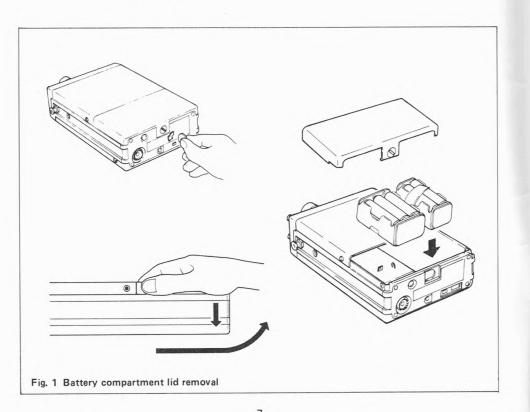
## 4.1 INSTALLING BATTERY PACKS

Install the specified battery packs in the unit's battery compartment of the unit as follows:

- Loosen screw (21) on the rear of the unit with a coin and open the battery compartment cover (see Fig. ).
- The supplied battery holders are designed to hold 6 and 4 battery cells each. The smaller holder has a dummy cell in it.
- Use nine manganese or alkaline battery cells (UM-3 1.5V) with the dummy cell left installed in the smaller holder.
- Use ten rechargeable Ni-Cad battery cells (1.2V) with the dummy cell removed from the smaller battery holder.
- After mounting batteries in the battery holders, install the holders in the unit's battery compartment as illustrated below. Close the compartment lid and tighten screw (21) with a coin.

# NOTE:

When using UM-3 battery cells (1.5V), leave the dummy cell in the smaller battery holder.



## HANDLING PRECAUTIONS FOR DRY CELLS

Careless handling of dry cells may result in electrolyte leakage or bursting. Note the following points.

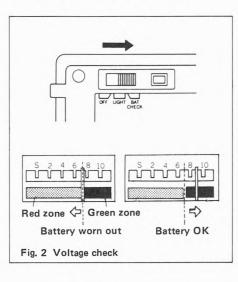
- When installing, be certain that their polarity is correct.
- 2. Do not mix new and used battery cells.
- Some types of dry cells with similar shapes may have different voltages. Be sure to use the same type of battery cells.
- Some types of dry cells are rechargeable while other types are not. Carefully read the notes on the cells before use.

# 4.2 SUPPLY VOLTAGE CHECK

Check the supply voltage as follows:

- 1. Set the LIGHT/BAT CHECK switch (19) to the BAT CHECK position.
- Turn the POWER/VOL control clockwise until the POWER switch clicks on. The lamps will glow and the meter will indicate the voltage supplied from the battery.
- 3. If the meter pointer is in the red zone on the meter scale (Fig. ), the battery cells require replacement or recharding. When the battery cells installed in the unit are of UM-3 manganese or alkaline type, replace them. If they are rechargeable Ni-Cad cells, recharge them.

When the transceiver is powered from an external power source, the meter will indicate the external source voltage.

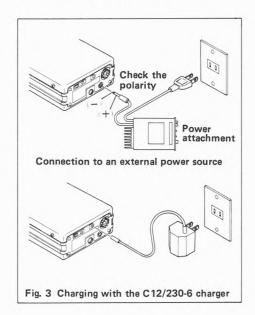


## 4.3 RECHARGING Ni-Cad BATTERY CELLS

- Ni-Cad battery cells used in the unit require recharging before the meter pointer falls into the red zone.
- When recharging the battery, be sure to turn the POWER/VOL control fully counterclockwise to the OFF position.
- Plug the optional charger into the charging socket (22) on C78 for charging. Optional charger: C12/230-6: 10 hours for 80% charging

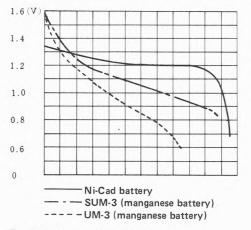
# CAUTION:

- 1. Do not try to recharge unrechargeable batteries such as UM-3 manganese or alkaline cells.
- 2. Avoid overcharging, as it shortens battery life.
- For normal charing efficiency, the Ni-Cad cells should be charged under an environment temperature of more than  $0^{\circ}$ C.



# 4.4 BATTERY TYPES AND THEIR CHAR-ACTERISTICS

Different types of batteries have different discharging characteristics (see Fig. 4). The manganese and alkaline batteries have a relatively linear discharging characteristic, while the Ni-Cad battery has a sudden voltage drop after it maintaining a relatively constant output voltage level.





When used in the C78 transceiver, fully charged Ni-Cad batteries operate for approximately 4 hours for repetitions of one-minute transmission, one-minute reception, and 8-minute standby (with battery saver on). The manganese battery cells allow an operating duration of approximately 1.5 hours in the same operation mode.

# 4.5 PORTABLE OPERATION

- For portable operation, attach the supplied shoulder belt to the strapping posts on the unit, and replace the microphone hanger in a convenient position on the shoulder belt. For mounting instructions, see Fig. 5.
- 2. We recommend the use of the optional carrying case CLC8, which will protect your equipment from possible damage and provide better maneuverability.

# 4.6 MOBILE OPERATION

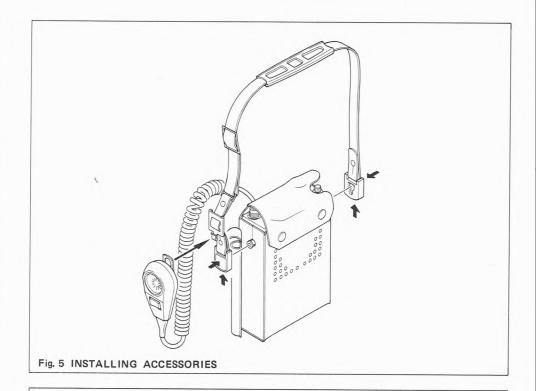
- For mobile operation, obtain the optional mobile bracket CMB8 from your dealer and mount it under the dash of your car. The unit can be popped in and out with the pop-out gear on the bracket (see Fig. 6).
- Combining the mobile bracket (CMB8) with the optional power booster CPB78, your transceiver is powered to 10 watts just by sliding the unit into the mobile bracket (see Fig. 6).

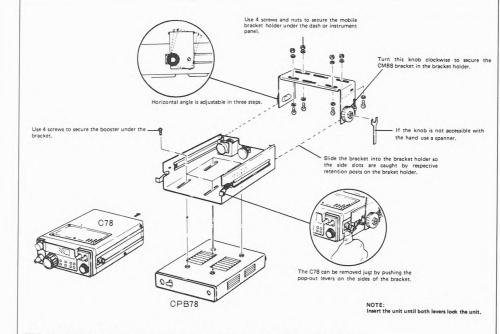
#### NOTE:

When operating the C78 transceiver in combination with the CPB78 power booster, be sure to set the power switches on both the C78 and CPB78 at the ON position. At the end of every operation, turn both power switches off.

- For mobile operation, it is adviseable to use the optional line filter (CLF03) in the power supply line to prevent noise interference from the alternator line.
- When using only the C78 transceiver for mobile operation, use the supplied external power plug for power connection to the lighter socket and install in it a fuse with a 1A rating.

When the transceiver unit is used in the mobile bracket (CMB8), connect the supplied power cable to the socket.





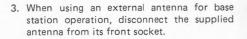
#### FIG. 6 INSTALLING MOBILE BRACKET AND POWER BOOSTER

# 4.7 BASE STATION OPERATION

- When the unit is to be powered from a commercial AC power source via a regulated DC power supply, use the supplied external power plug for connection and install in it a fuse with a 1A rating.
- When the transceiver unit is used with the power booster (CPB78), the unit should be powered through the power cable attached to the booster.

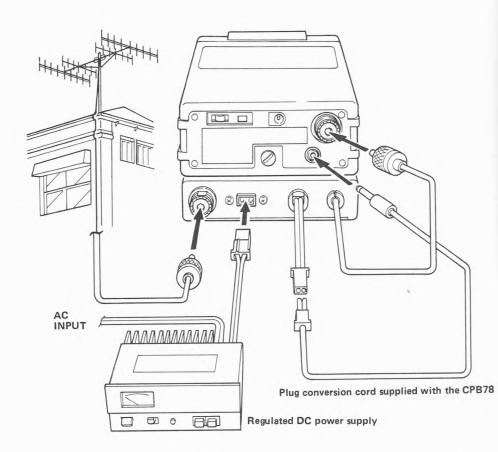
# NOTE:

The battery charger cannot be used as an external power supply.



## NOTE:

After a prolonged transmission interval, the power booster (CPB78) will become considerably hot, but this is normal. Allow free circulation of air around the unit for adequate ventialtion.



## 5. MICROCOMPUTER-ASSISTED OPERATION

## 5.1 MICROCOMPUTER OPERATION

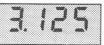
# 5.1.1 SELECTING A CHENNEL SCAN STEPPING

The desired channel scan stepping of 25 or 50kHz/step can be selected with the STEP switch located behind the battery compartment lid. This switching may be done with the unit left on.

# 5.1.2 MEMORY BACK-UP

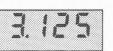
- The memory BACK UP switch behind the battery compartment lid is factory set at the OFF position.
- b. When the unit is turned on for the first time, a chennel frequency of 433.500 MHz is displayed in the frequency readout under the program control.
- c. When the BACK UP switch is turned on and the POWER switch is turned off, the channel frequency displayed just before the POWER switch is turned off is stored in the memory. When the POWER switch is turned on again, the same frequency is displayed in the frequency readout. (The scan mode is not stored.) This memory back-up feature is also effective when the unit is powered from an external power source.

[Example] When a channel frequency of 433.125MHz is displayed:



 $\rightarrow$  POWER switch turned off.  $\rightarrow$ 

POWER switch turned on.  $\rightarrow$ 



. .

## 5.1.3 FREQUENCY SELECTION

Channel frequencies can be selected with the CHANNEL SELECTOR on the front panel of the unit or with the UP-DOWN control on the mic:

- a. The Hand Microphone (MP-716) supplied with the C78 Transceiver is equipped with a channel frequency UP-DOWN control. Pressing and holding the control switch in the UP or DOWN position scans the channels (at a speed of 120ms/channel).
- b. When the UP-DOWN seitch is released, channel scanning stops at the frequency currently being received.
- c. Holding the UP-DOWN control switch for less than 0.5 second scans to the next channel where it stops.
- d. When the UP-DOWN control switch is activated, all other key operations are disabled, except for the press-to-talk (PTT) button on the microphone which stops channel scanning.

## 5.1.4 PROGRAMMING CHANNEL FREQUENCIES

## Initial frequency programming

The C78 Transceiver has 5 channel memory addresses (M1-M5) and one offset memory address. The offset memory stores frequency shift span data. To store frequency data in each memory address, follow the programming steps described below:

The C78 incorporates five memory units M1, M2, M3, M4, and M5, each capable of storing up to one frequency i.e. six frequencies in all. To store the desired frequency in each memory unit, follow the procedure given below:

# Storing a frequency datum in memory M1

 a. Press RCL key to access memory M1.
 (The channel frequency set up just before RCL key depression is 433.000MHz.)
 When no frequency datum is stored in M1:



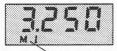
- (Memory indicator "M" blinks and memory address indicator "1" goes on both just below the MSD, indicating that no fre-
- b. Select the desired channel frequency with the CHANNEL selector on the unit or the UP-DOWN switch on the microphone (433.250MHz).

The selected frequency appears:

quency datum is present in M1).

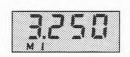


c. Press the ENTER key to store the displayed frequency datum in memory M1. Memory indicator M will go on.



M Goes on.

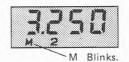
d. Press the RCL key to check the frequency datum stored in memory M1



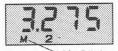
# Storing a frequency atum in memory M2

 Press the RCL key again to access memory M2.

When no frequency datum is stored:

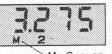


b. Select the desired channel frequency with the CHANNEL selector or the UP-DOWN switch (e.g. 433.275MHz).



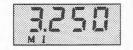
M Blinks.

c. To store the datum in memory M2 press the ENTER key. Memory indicator M goes on.



M Goes on.

d. Press the RCL key to check the frequency datum tored in memory M1.



e. Press the <u>RCL</u> key again to check the frequency datum stored in memory M2.

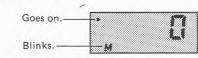


Storing frequency data in memory address M3 ~ M5

The desired channel frequency data can be stored in memory addresses M3  $\sim$  M5 in much the same way as the programming procedure for M1 and M2 described above.

Storing an offset datum in the offset memory

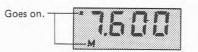
 Press the <u>RCL</u> key 6 times to access the offset memory.



Select the desired frequency shift span with the MHz button and the CHANNEL selector or UP-DOWN switch (e.g. 7.6 MHz of shift span).



c. To store the datum in the offset memory, press the ENTER key. After storing the datum, ensure press the CCL key.



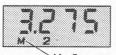
d. Press the **RCL** key 6 times to check the shift span datum stored in the offset memory.

## 5.1.5 STORED FREQUENCY ALTERATION

#### [Example]

Changing channel frequency stored in M2 (this example shows stored frequency alteration from 433.275 to 433.850 MHz.)

 Press the <u>RCL</u> key repeatedly until memory M2 is accessed.
 When frequency 433.275 MHz is stored:

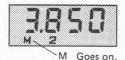


M Goes on.

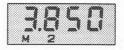
 Select the desired replacement frequency with the CHANNEL selector or the UP-DOWN switch (e.g. 433.850MHz)



c. To store the frequency datum in M2 press the ENTER key.

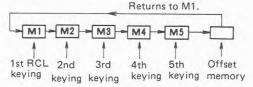


d. Press the RCL key twice to check the frequency datum stored in M2.



# 5.1.6 RECALLING STORED FREQUENCY DATA

a. Pressing the RCL key initially recalls a frequency datum tored in memory M1. Pressing it a second time recalls a frequency datum stored in M2. Each time the RCL key is depressed, frequency data are recalled sequentially from memory addresses M1 through M5 and the offset memory. When the RCL key is pressed after the contents of the offset memory are recalled, memory address M1 is again accessed.



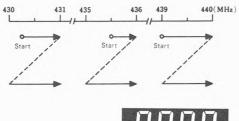
- The recall operation takes higher priority over CHANNEL selector and scanning operation.
- b. When the memory contents are recalled on the display by RCL operation, press the CCL key to clear the RCL function and bring back the data displayed before the RCL key was depressed.

## 5.1.7 FREQUENCY SCANNING

# How to scan the entire frequency band

 $\fbox{A}$  frequency scanning includes two different approaches: one divides the frequency range from 430  $\sim$  439 MHz into ten 1 MHz bands and scans within each of those 1 MHz bands, and the other is memory frequency scanning. The scan stop mode includes three different types.

- a. Scanning sub-band frequencies:
  - \* Select the desired sub-band to be scanned with the MHz key.
  - \* Press the ALL key to start scanning from the selected frequency.
  - \* Each time the <u>MHz</u> key is pressed during scanning, channel scanning is shifted to an upper sub-band.





During scanning, scan indicator "S" appears just below the LSD of the frequency readout.

# SCANNING TIME

Channel switch	50kHz stepping (1MHz) 20 channels	25kHz stepping (1MHz) 40 channels
Busy Vacant	Approx. 5 sec.	Approx. 10 sec.
Free	Approx. 40 sec.	Approx. 1 min. 20 sec.

b. To search for a busy channel:

BUSY-

VACANT-

- \* Set the SCAN MODE switch to BUSY.
- \* Adjust the SQL control.
- \* Scan stops at a busy channel where an input signal is present.
- \* When the input signal is ceases, scanning recommences.



- scan stop interval.
- \* Once the transceiver is set to the transmission mode by pressing the PTT button, channel scan will not start even when the input signal ceases.



- c. To search for a vacant channel:
  - \* Set the SCAN MODE switch to VA-CANT.
  - \* Adjust the SQL control.
  - Scan stops at a channel where no input signal is present.
  - \* When a signal is received on the channel, scan recommences.



Also remains on during the scan stop interval.

\* Once the transmission mode is temporarily activated by pressing the PTT button, channel scan will `not start even when a signal is received.



- d. To scan sub-band channels without interruption:
  - \* Set the SCAN MODE switch to FREE.
     \* Adjust the SQL control.
  - \* Channel scanning will go on at a rate of 2.0 sec per channel with no regard to input signal presence or absence.
- e. To stop scanning:
  - \* Press the CCL key.
  - \* Or press the Press-To-Talk button to momentarily activate the transmission mode.

# Stored channel scanning

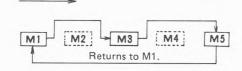
 a. When the MEMO key is pressed, channel frequencies stored in memory address M1 ~ M5 are scanned sequentially.



 When frequency data are stored in all the 5 memory addresses, scanning takes place as follows:



- Returns to M1.
- \* When no frequency datum is stored in M2 or M4, these memory addresses are skipped as follows:



Scan

b. To search for a busy or vacant memory channel or to scan all memory channels consecutively, select the appropriate SCAN MODE switch position in the same manner as in sub-band scanning.

# 5.1.8 OTHER USEFUL OPERATIONS

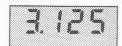
 Using the <u>RCL</u> and <u>CCL</u> Keys, you can recall a frequency stored in M1 and other arbitrarily selected frequencies alternately.

# [Example]

When frequency 433.250MHz is held by M1 and another frequency 433.125MHz is selected with the CHANNEL selector or the UP-DOWN switch:







- b. Channel scanning can be stopped by momentarily pressing the Press-To-Talk button during sub-band or memory scan. Utilizing this fact, you can stop channel scanning exactly when the desired channel or searched-for station is picked up, just by pushing the PTT button.
- c. The UP-DOWN switch on the microphone permits you manual channel scanning on a step-by-step basis.
- d. Combining the <u>MHz</u> key, CHANNEL selector and UP-DOWN switch, you can quickly locate the desired channel within a 10MHz bandwidth.

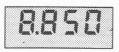
## 5.1.9 REPEATER OPERATION

Shifting the transmitter frequency for repeater operation:

- 1. Store the desired shift frequency span in the offset memory (e.g. 7.6MHz.)
- Select the desired reception channel frequency with MHz key and the CHANNEL selector or UP-DOWN switch.



- 3. Set the RPT switch to R1.
- 4. When the PTT button is pressed to activate the transmission mode, the transmission frequency is automatically shifted by the programmed frequency span (7.6MHz) as follows:



## NOTE:

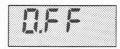
The transmission frequency is the sum of the reception frequency and the programmed frequency shift span. If it is shifted outside the amateur FM band, the frequency readout is "OFF".

# [Example]

Shift span: 7.6MHz Reception frequency: Transmission frequency:

ey: 434.000MHz ency: Shifted outside the amateur band by 1.6MHz.

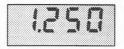
Frequency display:



In this state, the transceiver provides no emission.

# Shifting the reception frequency for repetition

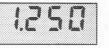
- 1. Store the desired frequency shift span in the offset memory (e.g. 7.6 MHz).
- Select the desired transmission channel frequency with the <u>MHz</u> key, CHANNEL selector of the UP-DOWN switch.



3. Set the RPT switch to R2. The reception channel frequency is obtained as follows (with 7.6MHz shift):



 When the PTT button is pressed, the selected transmission channel frequency is restored as follows:



## NOTE:

The reception channel frequency is the sum of the transmission channel frequency and the programmed frequency shift span. If it is shifted outside the amateur band, the frequency eadout is "OFF".

#### [Example] Shift span:

Shift span: 7.6MHz Transmission frequency: 434.000MHz Reception frequency: Shifted outside the amateur band by 1.6MHz. Frequency display:



When the reception frequency is shifted back within the amateur band with the MHz key, CHANNEL selector or UP-DOWN switch, the OFF message ceases and normal frequency display is restored in the readout.

## 5.2 OPERATING INSTRUCTIONS FOR RE-CEPTION

 Turn the VOL/OFF control clockwise to the detent "OFF" position.

The transceiver initially selects a channel frequency of 433.500MHz as long as the backed-up internal memory does not supply any frequency other than this.

- 2. Adjust the VOL control for the desired listening level.
- Adjust the SQL control (3) clockwise until the back-ground noise, heard over the speaker, just disappears.
- Select the desired channel by any of the following methods:
  - a. Operate the CHANNEL selector (4).
  - b. Operate the UP-DOWN switch (29).
  - c. Use the MHz key to select the desired 1 MHz sub-band to be scanned one of the 10 sub-bands from 430.000 to 439.975MHz, then press the ALL key to start scanning in 25 or 50kHz steps. The SCAN MODE switch (17) selects the following scan modes:
    - BUSY position: Scan stops at a busy channel.
    - VACANT position: Scan stops at a vacant channel.
    - \* FREE position: Scans all channels within the selected sub-band.
  - d. Press the SCAN MEMO key (11) to scan channel frequencies stored in the channel memory.
  - Press the MEMO RCL key (7) to recall a stored channel frequency.

For details, see (5.1.) MICROCOMPUTER-ASSISTED OPERATION.

#### 5.3 OPERATING INSTRUCTIONS FOR TRANSMISSION

## PRECAUTION

There are numerous radio channels in service close to the amateur band. If your mobile station is in the close vicinity of one such service station, the emission from your rig may cause unexpected interference to the service communications even if your emission is completely in compliance with regulations. Avoid operation in the following locations:

- 1. Check that your transmission will cause no trouble to other communications.
- Press the Press-To-Talk button and hold while you are transmitting. Speak distinctly into the microphone while holding it 3 ~ 5 cm away from your mouth.

# 5.4 ADJUSTING AUDIBLE KEYING TONE LEVEL

The keying tone level adjustment is located just below the speaker in the cabinet. For level adjustment, remove the top cover from the unit. Adjust the variable resistor located closer to the front panel on the PC board, with a Philips screwdriver.

This adjustment should be done with the squelch control activated.

# 5.5 RESETTING THE MICROCOMPUTER

If a malfunction occurs or key operation is ineffective, the internal microcomputer requires resetting.

To reset the microcomputer, press the rear RESET button (20) while the transceiver is on. Upon resetting, the frequency display will return to the initial frequency of "3.500".

# 6. RF ATTENUATOR MODIFICATION FOR FOX HUNTING

The C78 provides full maneuverability in outdoor portable operations. Particularly in fox hunting, the transceiver's receiver sensitivity is usually adjusted by connecting or disconnecting its antenna. If the receiver sensitivity is still too high even when the antenna is disconnected from the unit, it can be reduced by approximately 35dB by simple circuit modification and addition of an external variable resistor as follows:

# Modification Procedure

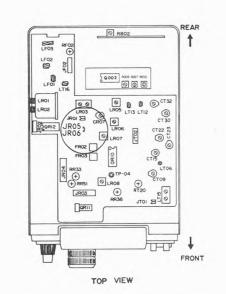
- Remove the top cover from the unit (with the speaker).
- 2. Cut the jumper across JR05 and JR06 at the middle of the lead.
- 3. Solder lead wires to the free ends of the jumper leads.
- 4. Connect the other ends of the leads to a variable resistor of 10  $\sim$  20 k $\Omega$  as shown at right.

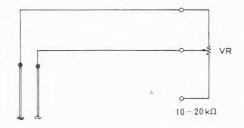
# 7. OPTIONAL FEATURES

The following optional features are available with the C78 for increased operation flexibility and convenience:

Mobile bracket	CMB8
10 watt booster	CPB78
Carrying case	CLC8
Charger	C12/230-6
Line filter	CLF03

Base station power supply CPS02





# SERVICE MANUAL SECTION OPERATING INSTRUCTIONS

## RECEIVER SECTION

- \* Reception system: Double conversion superheterodyne system using 21.4MHz for the 1st IF, and 455kHz for the 2nd IF.
- \* The input signal from the antenna terminal (J804) goes through an antenna switch circuit and a cavity, LR01 and LR02. The output of the cavity couples to gate 1 of QR01 (MOS FET), where it is amplified.
- \* The output of QR01 goes through another cavity consisting of LR03 and is fed to gate 1 of the 1st mixer QR02 (MOS-FET). Gate 2 of QR02 accepts the local signal (408.6MHz-418.58MHz) from the PLL board. The local signal is fed to JR01 and then fed to QR02 via LR04.
- The 21.4MHz output of QR02 goes through a monolithic filter, FR01, for better selectivity and intermodulation rejection.
- The output of the filter (FR01) is amplified by QR03 before being applied to pin 16 of IC QR10.
- \* QR10 (MC3357P) contains the 2nd local oscillator, 2nd mixer, IF amplifier, squelch switch, and detector.
- \* The 21.4MHz signal coupled to QR10 is converted to a 2nd IF signal, 455kHz, which passes through a dual stage ceramic filter that is externally provided across pins 3 and 5 of QR10. The output of the filter is internally amplified, subject to limiter detection, then coupled to pin 9 as an audio signal.
- \* The AF signal output goes to the preamplifier (QR06) and power amplifier (QR11). The output of the power amplifier drives the built-in speaker.

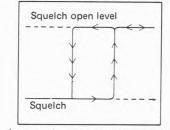
## [Squelch Circuit]

- \* The noise component present at pin 9 of QR10 goes through an active filter utilizing an internal amplifier. The output of the active filter is rectified by QR20 and QR21 (OA99). The rectified DC voltage couples to pin 12 of QR10 to control the Schumitttype squelch switch.
- \* When the squelch switch is activated due to the DC voltage applied to pin 12, pin 14 of QR10 is opened. When the squelch switch is turned off, pin 14 is shorted to ground.
- \* A + B voltage is applied to pin 14 or QR10 through RR40 (10kΩ). When the squelch switch is turned on, the emitter of the AF

preamplifier (QR06) and the muting terminal (pin 8) of the AF power amplifier (QR11) rise muting out the noise component.

\* The output voltage at pin 13 of QR10, used for scanning control, lowers when the squelch switch is on, and rises when the switch is off.

## [Schumitt-Type Squelch Circuit Operation]



Low - Input signal level -> High

[Meter Circuit]

\* The S meter drive signal present at pin 5 of QR10 is amplified by meter amplifiers, QR04 and QR05, and applied to meter M801 via JR04.

## TRANSMITTER SECTION

- \* The external microphone (MP716) output is amplified by QR401 and QR402. The amplifier output goes through a low-pass filter where frequency components above 3kHz are filtered out. The signal is then applied to Q203 in the VCO circuit to directly frequency modulate the VCO output (reactance modulation).
- \* The output of the PLL board (J102) is fed to JT01 on the transmission younger stage stage board.
- \* The signal supplied from the PLL board is amplified by QT01, QT02, QT03 and QT04 in this order, then is applied to antenna connector (J804) via low pass filter and antenna switch.
- The Automatic Power Control (APC) circuit utilizes the voltage feedback principle. The output of QT04 is rectified by diodes QT11 and QT12. The rectified DC voltage is applied to the base of the APC control transistor QT06, to control the 2nd gate voltage of QT01 (MOS FET) and hence RF power.
- \* When the APC is inactive, RF power is approximately 2 watts.

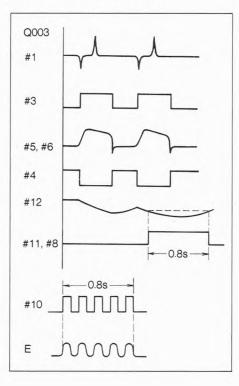
## **Tone Burst Generator**

# When the PTT switch is used:

When the PTT switch is pressed initially, the potential at terminal A in the schematic diagram lowers. This causes #1 of Q003 to lower momentarily, causing #3 of Q003 to rise. As a result, #5 and #6 of Q003 also rise, which lowers #4 of Q003. The potential at #12 and #13 of Q003 starts dropping but does not reach its lowest level, leaving the tone burst circuit inactive.

When the PTT switch is pressed twice consecutively, the potential at #12 and #13 of Q003 goes to its lowest level. This causes #11 of Q003 to rise, activating the tone burst generator. The potential at #12 and #13 of Q003 gradually increases, and #11 of Q003 is maintained at a high level for 0.8 seconds. This means that the tone burst signal is transmitted for only 0.8 seconds when the PTT switch is pressed a second time.

The output of the tone burst generator is level-adjusted by R010, then applied to the PLL modulator via R011 and C006.



# When the CALL button on the C78 is used:

- \* Pushing the CALL button applies a voltage, +9V, to terminal B in the schematic diagram. This brings up the potential at the base of Q001, turning it on and therefore causing terminal A to lower, putting transceiver in the TX mode.
- As a high level is applied to #5 and #6 of Q003 through Q002, #4 of Q003 is maintained at a low level while the CALL button is held down.

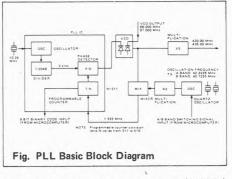
Number 12 and #13 of Q003 are lowered, raising # 11 and #8, which activates the tone-burst generator.

- \* When the CALL button is released, Q001 is turned off. This causes terminal A to rise, putting the transceiver in the RX mode.
- Q006, a voltage regulator, holds the voltage at Q003 at +9V.

# PLL SECTION

\* The PLL circuit in the C78 is controlled by 9-bit binary code and A/B band switching signal both coming from microcomputer QL01.

PLL Frequency Relationship at Transmission Mode in Bands A (430MHz) and B (435.00MHz)



 The VCO frequency is one fifth (80MHz) the object transmission and reception frequency.

The 80MHz band signal oscillated by the VCO is multiplied by 5 in the PLL.

For transmission, the object frequency is obtained, while for reception, a frequency 21.4 MHz lower than the object frequency is obtained.

The A/B band switching signal is automatically supplied from the control IC (QL01) according to operation frequencies selected. The A/B band switching signal switches local oscillator frequencies inside the PLL. A band ..... 430.000 ~ 434.975 MHz B band ..... 435.000 ~ 439.975 MHz

1. PLL IC (Q113)

PLL IC Q113 integrates the following circuits on a single chip.

- \* Reference frequency oscillator: 10.24MHz \* Phase Detector: P/D
- \* Programmable Counter: I/N
- \* Unlock Detector

# (1) PROGRAMMABLE COUNTER (1/N)

The programmable counter accepts a 9-bit binary code from the microprocessor (QLO1) which determines the frequency division ratio. The output frequency from the mixer is divided by the programmable counter in accordance with the determined division ratio, and the divided frequency is fed to the phase detector.

# (2) PHASE DETECTOR (P/D)

- The phase detector detects the phase difference between 5kHz frequency, which is obtained by dividing the reference frequency of 10.24MHz by 2048, and the programmable counter output frequency.
- 2. The detector output obtained at pin 7 is converted into a DC voltage by an R/C integrating circuit. This DC voltage is applied to a varicap Diode (Q201) in the VCO to control the VCO output frequency. This DC voltage variation applies to the varicap diode which controls the VCO output frequency.

# 2. VCO CIRCUIT

- 1. The phase detector output from PLL IC Q113 is converted into a DC voltage by an R/C integrate circuit.
- 2. The DC voltage is applied to varicap diode Q201 to vary its capacitance.
- The VCO output frequency is controlled by the variation of the varicap diode capacitance.
- The VCO output frequency covers a maximum variation range of 3MHz according to input DC voltage variation.

# 3. LOCAL OSCILLATOR

- The local oscillator uses the overtone oscillation system. It oscillates at the following frequencies. frequencies:
  - RX-S 40.0825MHz in band A RX-S 40.5825MHz in band B
  - TX-S 42.2225MHz in band A
  - TX-S 42.7225MHz in band B
- Each of the above frequencies is doubled before they are coupled to mixer: RX-S 80.165MHz in band A RX-S 81.165MHz in band B TX-S 84.445MHz in band A TX-S 85.445MHz in band B

## 4. MIXER

- 1. The output of the VCO circuit is fed to the mixer via buffer amplifier Q205 and Q117.
- The VCO output and local oscillator output are mixed in mixer to create frequencies from 1.555 ~ 2.550 MHz.

F(VCO) - F (Local OSC) = 1.555 ~ 2.550 MHz.

 The mixer output goes through an LPF, Q101 and Q102 to be subject to waveform shaping before it is fed to Q113's pin 2 (1/N circuit).

## 5. PLL IC (Q113) UNLOCK

An unlock signal is obtained at pin 8 of the phase detector circuit in the PLL IC.

## 6. UNLOCK SWITCH CIRCUIT

The unlock output of the PLL IC is integrated into a DC voltage by a C/R circuit. Using this DC voltage, Q115 discriminates between locked and unlocked the PLL state.

#### 7. UNLOCK PREVENTION CIRCUIT

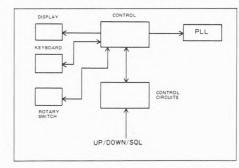
In order to prevent faulty VCO circuit operation, a UL (unlock) voltage is applied from Q116 to the varicap (Q201) in the VCO circuit if the PLL circuit is unlocked. This maintains the VCO circuit within its stable operation range.

Frequency	Dividing	Binary Code								
Display (MHz)	Ratio	R <sub>o</sub>	R,	R <sub>2</sub>	R,	R4	R <sub>s</sub>	R <sub>6</sub>	R,	R,
430.000	311	1	1	1	0	1	1	0	0	1
430.025	312	0	0	0	1	1	1	0	0	1
430.050	313	1	0	0	1	1	1	0	0	1
434.975	510	0	1	1	1	1	1	1	1	1
	Band	Swit	ching	take	s plac	e.				
435.000	311	1	1	1	0	1	1	0	0	1
435.025	312	0	0	0	1	1	1	0	0	1
435.050	313	1	0	0	1	1	1	0	0	1
:							-			
439.975	510	0	1	1	1	1	1	1	1	1
	Band	Swit	ching	take	s plac	e.				_
430.000	311	1	1	1	0	1	1	0	0	1

# CONTROL SECTION

The control section consists of the following sub sections:

- \* Microprocessor section
- \* Display section
- \* keyboard section
- \* Channel switching section (manual)
- \* Control I/O decoder section
- \* Control LSI QL01 operates on a supply voltage +4.5 to +5.5V. With the C78, it operates on a supply voltage +5V.



1. THE FOLLOWING OUTPUT SIGNALS ARE AVAILABLE FOR EXTERNAL CIRCUIT CONTROL

 PLL IC programmable counter drive output: 9-bit binary code output for programmable counter drive is available at pins (22) through (30).

(2) LCD Drive

An LCD drive output (dynamic) is available at pins 8 through 13.  $(D_0 \sim D_3, E_0, E_1)$ 

(3) Buzzer Drive

Output F1 at pin 17 conttols the buzzer tone generator provided within QL02.

(4) Battery Saver Control Output

When the BATT SAVE switch is turned on, output F0 at pin 16 provides a pulse array with a duty cycle of 1/8 to switch the RX+B control transistor, QS16, accordingly.

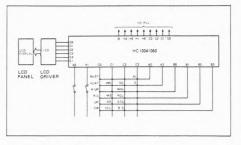
# 2. THE LSI REQUIRES THE FOLLOWING COMMANDS.

(1) Initial clear [pin 7]

A positive pulse is applied to pin 7 (RES) of the control LSI so as to reset all internal circuits to their initial state when the power to the unit is turned on.

(2) Matrix circuit [pins 2 - 5 and 35 - 40] The matrix circuit permits up to 18 key inputs.

Pin  $2(C_0)$ ,  $3(C_1)$ ,  $4(C_2)$ ,  $5(C_3)$ ,  $35(A_2)$ ,  $36(A_3)$ ,  $37(B_0)$ ,  $38(B_1)$ ,  $39(B_2)$ ,  $40(B_3)$ 



# **Channel Switching**

\* CHANNEL Knob

This rotary switch utilizes a pulse switch, which closes matrix C0  $\sim$  B0 for up-going channel selection, and matrix C0  $\sim$  B1 for down-going channel selection.

\* UP-DOWN switch on the microphone The UP-DOWN switch on the microphone controls internal analog switches, which close matrixes C0 ~ B2 and C0 ~ B3 which control channel frequencies in the respective directions. The up/down command from the microphone switch is applied to pins 13 and 5 of QL03.

#### **Key Operation**

## 1. MHz

Pressing the MHz key closes matrixes C2 and B0. Each depression of the MHz key increments the operation frequency by 1MHz. When the MHz key is pressed and held, channel frequency automatically steps up at a rate of 2MHz/sec.

## [Example]

Original frequency:	433.000MHz
1st depression:	434.000
2nd depression:	435.000
3rd depression:	436.000
	pression causes the fre-
quency to	return to 430.000.
7th depression:	430.000

#### 2. BATT/SAVE

The BATT/SAVE feature is activated by closing matrixes C2 and B3. When it is activated, a small dot appears at the right margin of the LCD display, and output F0 at pin 16 provides a pulse array with a duty of 1/8 to control RX + B.

When the BATT/SAVE feature is inactive, output F0 is maintained at a high level.

If analog switch C2 – A3 for SQL control is closed or scan control key (AS or MS) is pressed when the BATT/SAVE feature is activated, output F0 is also maintained at a high level.

## 3. CCL

Pressing the CCL key closes matrixes C1 and B3. This resets the MEMO, RCL, SCAN ALL and SCAN MEMO features to their initial state.

## 4. SCAN MEMO

Pressing the SCAN-MEMO key closes matrixes C1 and B1. This scans the 5 stored channel frequencies in sequential order.

# 5. SCAN ALL

Pressing the SCAN-ALL key closes martixes C1 and B2. This starts channel scanning upward from the displayed frequency with a spacing of 1 MHz.

## 6. MEMO-RCL

Pressing this key closes matrixes C2 and B1. This recalls the stored frequency to the display.

## 7. MEMO-ENTER

Pressing the MEMO-ENTER key closes matrixes C2 and B2. This stores the displayed frequency in memory.

## Slide Switch Function

## 1. VACANT SCAN

The vacant scan mode is activated when matrixes C0 and A3 are closed.

## 2. BUSY SCAN

The busy scan mode is activated when matrixes C0 and A2 are closed.

## 3. FREE SCAN

The free scan mode is activated when both matrixes Co - A3 and CO - A2 are opened. Scan rate is automatically set high for the busy and vacant mode, and set low for the free mode.

# 4. S (Simplex)

When matrixes C3 and A3 are closed, the RPT mode is set to the simplex (S).

# 5. R1

When matrixes C3 and A2 are closed, the RPT mode is set to R1. In this mode, the transmission frequency is shifted up by the frequency data stored in the offset memory.

# 6. R2

When matrixes C3 - A3 and C3 - A2 are opened, the RPT mode is set to R2. In this mode, the reception frequency is shifted up by the frequency span data stored in the offset memory.

## [Example]

Landinproj	
Frequency selected	435.000 MHz
Offset memory data	3.00 MHz
R1{Tx	438.00 MHz
Rx	435.00 MHz
R2/Tx	435.00 MHz
Bx	438.00 MHz

## Scan Operation Sequence

- a. In the busy scan mode, scanning stops at a channel where an input signal is present.
  - \* When an input signal is present, an high level output is provided at pin 3 of QR12.
  - \* The high level output at pin 3 of QR12 couples to pin 6 of QL03 to close matrix C2 A3. The microprocessor detects this closure of the matrix and stops scanning. In the busy scan mode, scanning stops when matrix C2 A3 is closed.
- b. In the vacant scan mode, scanning stops at a channel where no input signal is present.
  - \* When no input signal is present, pin 3 of QR12 provides a low level output. As a result, matrix C2 – A3 remains open if the low level output at pin 3 is coupled to pin 6 of QL03.
  - \* In the vacant scan mode, scanning stops when matrix C2 A3 is opened.
- c. The microprocessor monitors the scan modes (busy, free, vacant) and matrix C2 – A3 states to provide the following controls: In the busy mode: Scan stops when C2 –
  - In the Free mode: Scan stops when C2 A3 is closed. Scan starts when C2 – A3 is opened. Scan continues regardless of matrix states.

In the vacant mode: Scan starts when C2 — A3 is closed. Scan stops when C2 — A3 is opened.

.

# 25/50kHz Channel Stepping Selection

Channel stepping is selected with the channel stepping selector switch SL01. Closing matrix C1 - A3 selects 25kHz stepping. Opening matrix C1 - A3 selects 50kHz stepping.

## (1) Memory Back-Up

- a. The SW + B is monitored at pin 33 (A0) of QL01.
- b. When the C78 is switched off, the potential at A0 lowers.
- c. When A0 lowers, the microprocessor lowers all its outputs.
- d. The memory is backed up since the microprocessor remains alive even when its A0 is low.

## (2) Control Section State in the TX Mode

In the TX mode, TX + B is applied to A1 to render all input to the microprocessor ineffective and hold all output at a DC level. Since in this state no pulse enters or leaves the microprocessor, the signal-to-noise ratio in the TX mode is significantly improved.

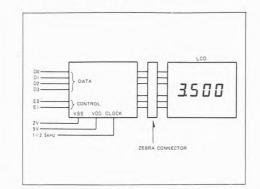
## 3. DISPLAY SECTION

The display section consists only of an LCD and LCD driver.

Four data lines (D0  $\sim$  D3) and two control lines (E0 and E1) are coupled from QL01 to the LCD driver (Q801). Power supply voltages VDD and VSS, and a clock signal are also applied to the LCD driver.

Based on these signals, the LCD driver dynamically drives the LCD display with 1/3 duty pulse.

The presence of dynamic drive means that the LCD driver requires a clock signal.



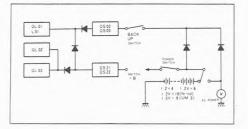
# 4. OTHER PERIPHERAL CIRCUITS

(1) Back-Up Circuit

When the C78 is switched on, necessary DC power is supplied by voltage regulator QS21.

When the C78 is switched off, the back-up power from the 12V battery goes through the back-up switch, SL02, and couples to the voltage regulator, QS02, where it is converted into +5V DC before being supplied to the memory.

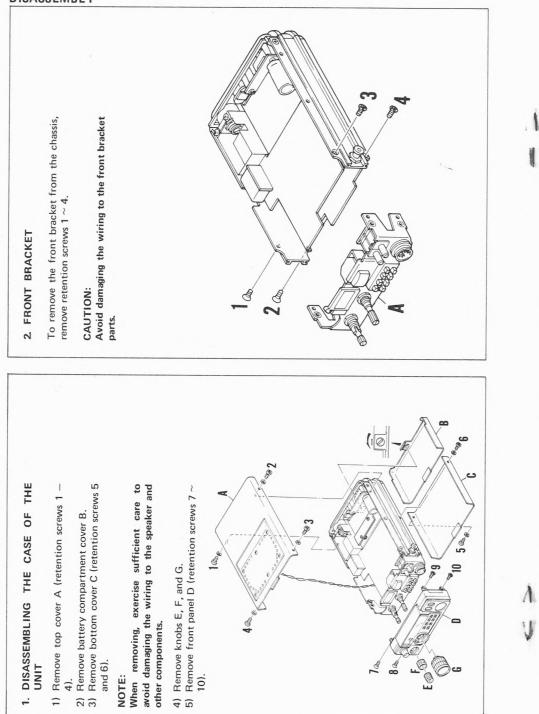
When an external power source is used, the microprocessor is also backed up as long as the BACK UP switch is on.

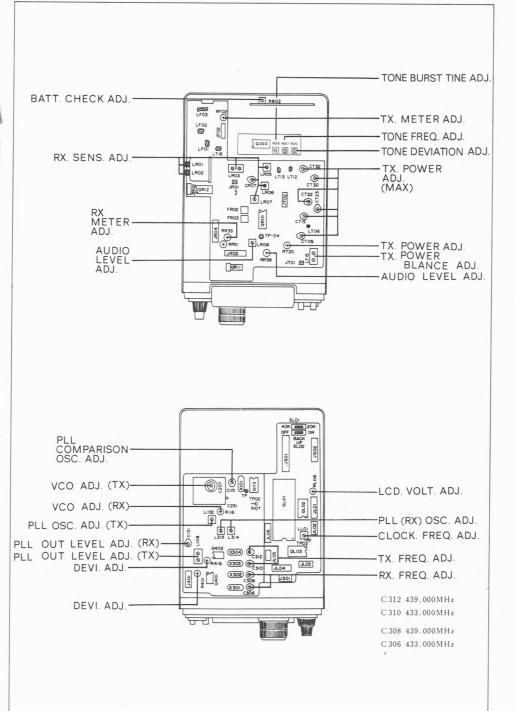


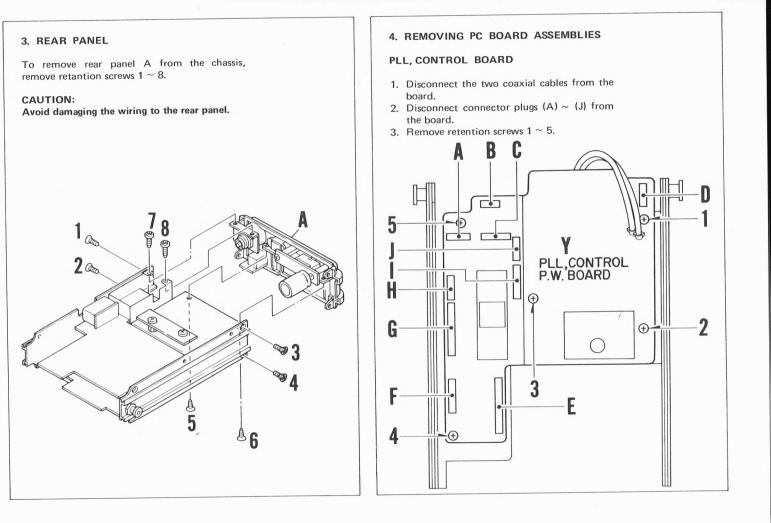
(2) Reset Circuit

When the RESET switch is pressed, the base of QL01 is grounded. This applies a +5V to pin 7 of QL01, thus resetting the entire internal circuits of QL01, including the memory.



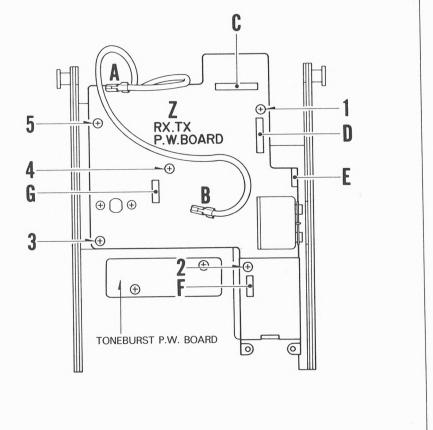






# RX, TX BOARD

- 1. Disconnect coaxial cables (A) and (B) from the board.
- 2. Disconnect connector plugs (C)  $\sim$  (G) from the board.
- 3. Remove retention screws 1  $\sim$  5.



# ALIGNMENT PROCEDURE

# CONDITIONS

- \* All adjustments have been completed prior to shipment. Further adjustments should be limited to a necessary minimum.
- Make sure that all measuring instruments required for alignment are completely calibrated and operate normally.
- \* Before starting measurement, idle the instruments for half-an-hour.

# **Required Measuring Instruments**

- 1. UHF standard signal generator
- 2. RF power meter
- 3. Audio signal generator
- 4. AC/DC voltmeter (VTVM)
- 5. RF voltmeter
- 6. Frequency counter
- 7. Oscilloscope
- 8. Galvanometer
- 9. Regulated DC power supply
- 10. DC ammeter
- 11. (Spectrum analyzer)
- 12. (Digital voltmeter)

## **Required Alignment Tools**

1.	Philips screwdriver	for casing and boards
	Standard screwdriver	
3.	Non-metallic standard	
	screwdriver	for RF and trimmer capacitor adjust- ment
4.	Box screwdriver	for support (2.6, 3.0mm)
	r RF circuit and frequer n-metallic screwdriver.	ncy adjustment, use a

# C78 ALIGNMENT PROCEDURE

# 1. Standard Alignment Conditions

Supply voltage:	13.8V DC
Audio output:	0.7 watts
Audio output loading:	8.0Ω
Frequency deviation:	±3.5kHz
Modulation frequency:	1kHz
Transmitter load:	50Ω
Reception frequency:	435.00MHz
Transmission frequency: .	435.20MHz

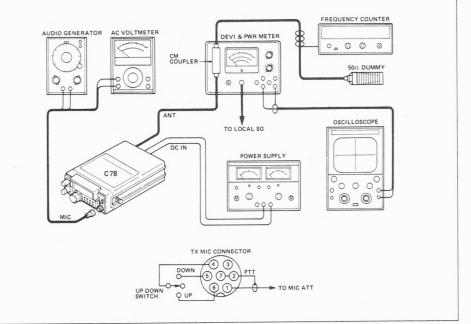
# 2. Alignment Notes

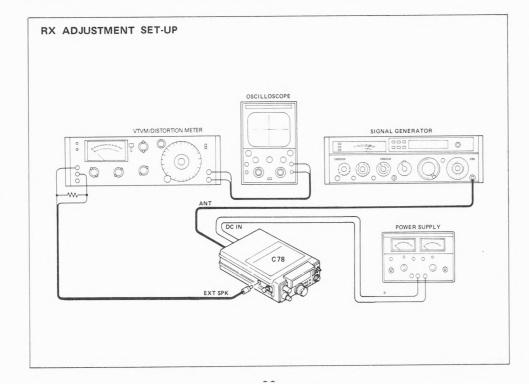
Handle all trimmer resistors and capacitors gently. Unless otherwise specified, set the switches and controls as follows:

1.	JUL	winimum	
2.	VOL:	Minimum or most	
		adequate level	
3.	SCAN MODE switch:	FREE	
4.	RPT switch:	S	
5.	CHANNEL STEP switch:		
		50kHz	
6.	Supply voltage:	13.8V DC	
7.	Frequency:	RX: 435.00MHz	
		TV 405 001411	









## MICROCOMPUTER SECTION

## LCD Supply Voltage Adjustment

- 1. Switch on C78.
- Connect a voltmeter across pins 2 and 3 of connector JL01. Adjust RL06 until the voltmeter reads 3.0V DC.

# NOTE:

- 1. Use a DC voltmeter with high accuracy.
- 2. Application of a voltage exceeding the LCD's rated voltage (3.5V) may damage the LCD.
- 3. Pins 3 and 2 of JL01 are positive and negative respectively.

# **Clock Frequency Adjustment**

- 1. Connect a frequency counter to TP03.
- 2. Adjust the frequency at TP03 to 400.0kHz with LL01.

# PLL SECTION

# NOTES:

- Unless otherwise specified, leave the PTT switch off during PLL section adjustment.
- 2. Complete PLL section adjustment before adjusting TX and RX.
- 3. During PLL section adjustment, disconnect the coaxial cables from jacks JR01 and JT01.

After completing the adjustment, reconnect them.

# PLL Comparison Oscillator Adjustment

1. Connect a frequency counter to TP101, and adjust the frequency at TP101 to 5.1200MHz with C115.

# Reference Frequency Adjustment

- 1. Connect a RF VTVM to TP02 (R107).
- 2. Set up C78 for a channel frequency readout of 430.00MHz.
- 3. In the RX mode, adjust L314 and L315 until the maximum level is obtained at TP02.
- Set up C78 for a frequency readout of 439.95MHz.
- 5. In the TX mode, adjust L102 until the maximum level is obtained at TP02.
- 6. Adjust L314 and L315 until there is no level difference at TP02 in the TX and RX modes. (Level: approx. 1.5Vrms)

# VCO Adjustment

- 1. Set up C78 for a channel frequency readout of 430.00 MHz.
- 2. Connect a voltmeter to C251.
- In the TX mode, adjust the voltage at C251 to 2.25V with C207.
   In the RX mode, adjust the voltage at C251
- to 2.0V with R116.

# Output Adjustment

- 1. Connect an RF VTVM with a 50Ω dummy load to the TX output coaxial.
- 2. When C78 is in the TX mode, the VTVM will read approximately 200mV (when the slug of L108 is turned clockwise by 2.5 turns into the coil bobbin).

# NOTE:

When L108 is replaced, turn its slug 2.5 turns into the bobbin in advance.

 Check that no output level difference exists between output frequencies 430.00 and 439.95MHz. If there are any, adjust the two slugs in L108 until the difference is eliminated. (The two slugs of L108 should be located at around the same level when adjustment is completed.)

## NOTE:

Turning the slug into the coil bobbin reduces the frequency, while turning it out of the coil bobbin increases the frequency.

- 4. Set C78 in the RX mode, and select a channel frequency of 435.000MHz.
- Connect an RF VTVM with a 50Ω dummy load to the RX output coaxial, and adjust C131 until a maximum reading is obtained in the RF VTVM.

# NOTE:

After completing the adjustment, connect the TX and RX output coaxials to JT01 and JR01 respectively.

# Frequency Alignment

- Disconnect the coaxial cable from JT01, and connect a frequency counter to the free end of the coaxial cable.
- Set up C78 for a channel frequency readout of 433.00MHz and for the TX mode. Adjust C310 until the frequency counter reading is 433.00MHz.
- Set up C78 for a channel frequency readout of 439.00MHz and for the TX mode. Adjust C312 until the frequency counter reading is 439.00 MHz.
- Disconnect the coaxial cable from JR01, and connect the frequency counter to the free end of the coaxial cable.
- Set up C78 for a channel frequency readout of 439.00MHz and for the RX mode. Adjust C308 until the frequency counter reading is 417.60MHz.
- Set up C78 for a frequency readout of 433.00MHz and for the RX mode. Adjust C306 until the frequency counter reading is 411.60MHz.
- 7. After completing the adjustment, connect the coaxial cables to their original jacks.

# TRANSMITTER SECTION

# **RF** Power Adjustment

- 1. Set C78 for a frequency readout of 435.20 MHz.
- 2. Turn the slug of LT15 by 2.5 turns into the coil bobbin. (Only when LT15 is replaced.)
- 3. Adjust CT09, CT15, CT22, CT23, CT32, and CT30 until the maximum RF power is obtained. In this case, leave RT20 in the fully clockwise position.
- 4. Adjust LT15 until the RF power difference between frequencies 430.00 and 439.95MHz is minimized.
- The maximum RF power will reach approx.
   1.8 watt. Reduce RF power to 1.3 watts with RT20.
- Increase the RF power by adjusting LT14, then again reduce the RF power to 1.3 watts with RT20.
- 7. Adjust LT15 until there is no RF power difference between both band edges.

# Deviation Adjustment

- 1. Turn R401 and R416 fully clockwise.
- Set up an audio signal generator output level for 30 mV AC and couple it to the MIC jack.

Set up the unit for the TX mode and adjust the maximum frequency deviation to  $\pm 5 kHz$  with R416.

- Set the AF generator output level to 3.0 mV (AC).
  - Set up the unit for the TX mode and adjust the frequency deviation to  $\pm 3.5 kHz$  with R401.
- Depress the CALL button and adjust R010 so that the tone deviation is ±3.5 kHz. Adjust R007 so that the tone frequency is 1750 kHz.
- After adjusting the frequency, depress the PTT switch twice and adjust R005 so that the function time of the tone burst circuitry is 0.8~1.2 seconds.

# NOTE:

The tone burst circuitry should not function when the PTT switch is depressed once. When the function time is adjusted with a jig, it is not necessary to adjust R005.

To measure the function time of the tone burst circuitry, connect an oscilloscope or synchroscope to one of the following. 1. To J402 and J403

- 2. To the speaker terminal of the receiver
- 3. To the AF output of a linear detector

## Meter Adjustment (TX)

 Adjust RF02 until the meter pointer deflects to position "9" at a TX output of 1.3 watt.

# RX SECTION

## Sensitivity Adjustment

- 1. Set the C78's channel frequency to 435.00MHz.
- Couple an RF signal generator output of 435.00MHz to the ANT jack on the C78.
- 3. Adjust LR01 and LR02 until C78's S meter pointer deflection is a maximum.

## NOTE:

# If the S meter pointer deflects only slightly, turn RR33 fully clockwise.

- Adjust LR03's slug until the S meter pointer deflection is a maximum.
- Adjust LR05, LR06, and LR07 until the S meter pointer deflection is a maximum. Adjust CR07 until the optimum SIND point is obtained.

## NOTE:

After replacing a trimmer capacitor or coil, adjust them until maximum noise output level is obtained.

- Repeat steps 1sto 5 several times. After the adjustment is completed, turn the slug of LR02 by 1/2 a turn into the coil bobbin.
- 7. Adjust LR08 until the maximum audio output level is obtained.
- 8. Adjust until no sensitivity difference exists between frequencies 430.00 and 439.95 MHz.

## NOTES:

- Set up the RF signal generator output for a modulation frequency of 1kHz with a deviation of 3.5 kHz, and set the output level to a minimum.
- 2. Adjust until no sensitivity difference exists between frequencies 430.00 and 439.95 MHz.
- 3. When using a center meter, connect a voltmeter to RX TP04, and obtain the point where the voltmeter reads the same level when a 455kHz signal is applied to the input and when a signal is received at the antenna. The 455kHz signal should be applied to pin 5 of QR10 via capacitor 0.001µF.

## Audio Output Level Adjustment

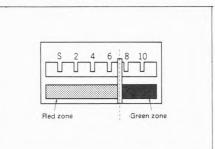
- Set up the RF signal generator output for a frequency of 435.00MHz, a modulation frequency of 1 KHz, deviation of 3.5 kHz, and a level of 60dB, and couple it to the ANT jack on the Unit.
- Connect the VTVM to the EXT. SPK jack, and adjust RR38 until the VTVM redds approx. 3.2 V (1.3 W).

# Meter Adjustment (S meter)

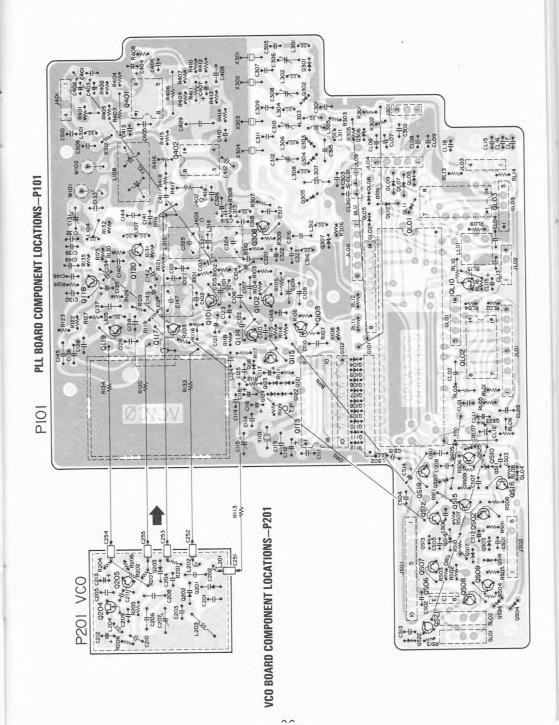
 After completing sensitivity adjustment, couple a 10dB non-modulated carrier signal from the RF to the ANT jack on the unti. Adjust RR33 until the S meter pointer indicates "6".

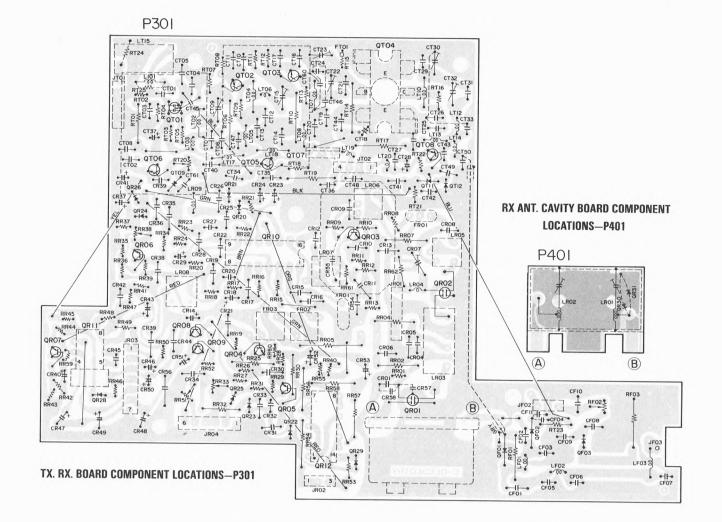
# OTHER ADJUSTMENTS

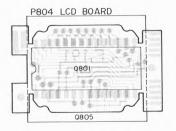
- Meter Adjustment (battery check)
- Set the rear slide switch to BATT CHECK.
   Set the supply voltage to 9.6V. Adjust R802 until the meter pointer deflects to the boundary of the red and green zones on the meter scale as shown below.



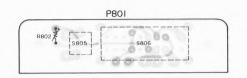
# COMPONENT LOCATIONS



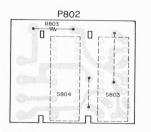




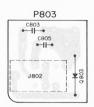
LCD BOARD COMPONENT LOCATIONS-P804



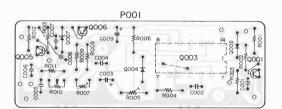
**REAR SWITCH BOARD COMPONENT LOCATIONS-P801** 



FRONT SWITCH BOARD COMPONENT LOCATIONS-P802

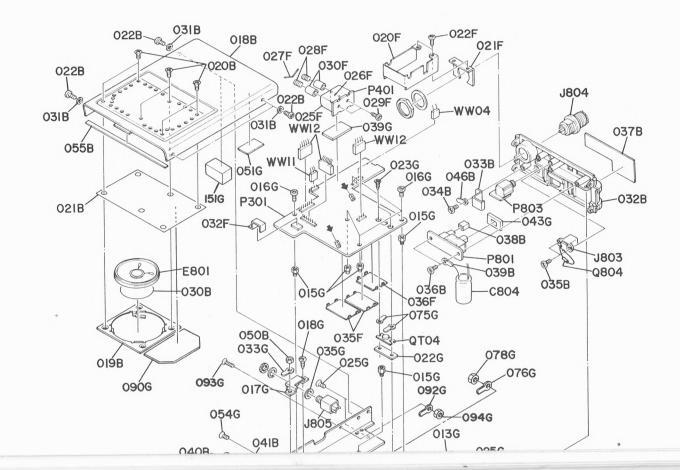


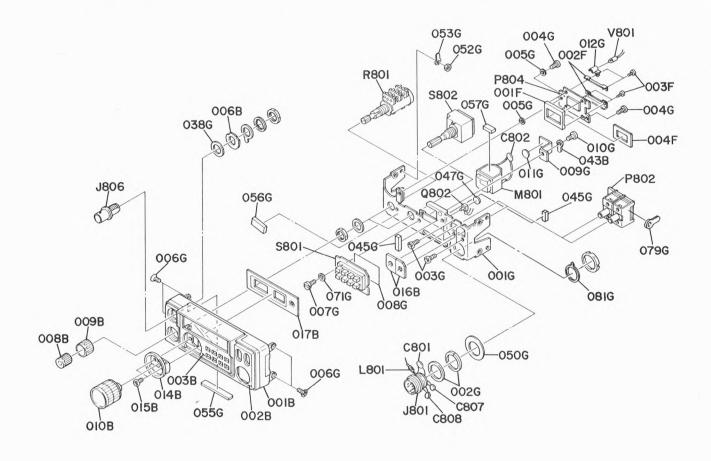
# **EXT. POWER BOARD COMPONENT LOCATIONS-P803**



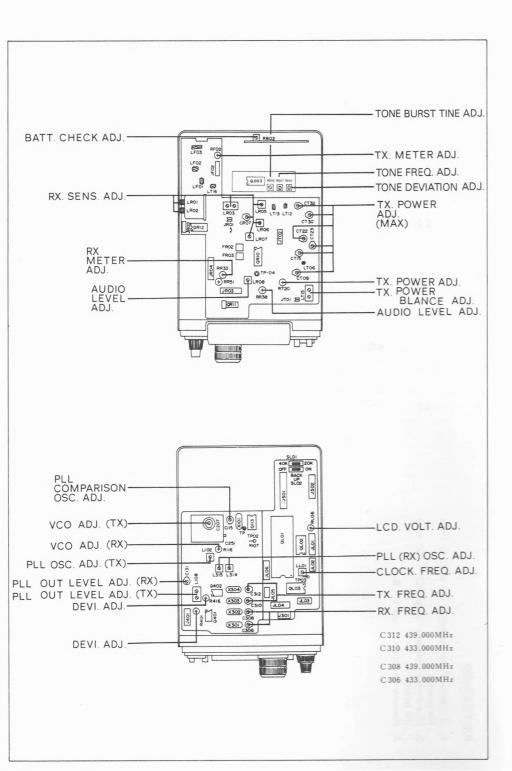
TONE-BURST BOARD COMPONENT LOCATIONS-P001

[M01-99] VARIOUS BOARDS AND COMMON PARTS





REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIP	TION	REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIPTION
001B	1	200C064010	Case, Front		055G	1	200C056060	Buffer
002B	1	203C063010	Escutcheon, Mould		056G	1	200C056070	Buffer
002B	1	200C063120	Escutcheon, Acryl		057G	1	200C056080	Buffer
017B	1	200C063030	Escutcheon, Alumi	Plate	071G	1	59264702G9	Washer
0170		2000000000	Escatemeon, Alam	1 late	079G	1	62031340W0	Lug
					081G	1	62150019E0	Lug
006B	1	203C114010	Stopper		001F	1	200C064060	Case, LCD
008B	1	4723154020	Knob, VOL		002F	2	200C005040	Clamper
009B	1	4723154030	Knob, SQL		003F	4	51400019K0	B.H. Tapped Screw
010B	1	200C154500	Knob, Channel		004F	1	200C053030	Cover
014B	1	200C353010	Ring		C801	1	DK16102300	Ceramic Cap. 1000pF ±10%
015B	2	51100205E0	B.H.M. Screw	B2 x 4	C802	1	DK16102300	Ceramic Cap. 1000pF ±10%
016B	2	200C118010	Spacer, Slide SW		C807	1	DD15470370	Ceramic 470pF ±5%
043B	1	62021030W0	Lug		C808	1	DD15470370	Ceramic 470pF ±5%
					L801	1	LC12010012	Choke Coil, 8T
001G	1	200C105010	Chassis, Front		M801	1	IM11020030	D.C. Meter
002G	2	4656118010	Spacer		0802	1	HI10009020	L.E.D.
003G	4	51062603A0	P.H.M. Screw	P2.6 x 3	R801	1	RD12030080	Variable Resistor $20K\Omega(A)$ - $20K\Omega(E)$
004G	2	51060203A0	P.H.M. Screw	P2 x 3	S801	1	SK08080020	Keyboard Switch
005G	2	59020403G0	Washer		S802	1	SR18020010	Rotary Switch
006G	4	51042604A0	F.H.M. Screw	F2.6 x 4	V801	1	IN10140080	Lamp, 40mA 14V
007G	1	200C005030	Clamper		J801	1	YJ10001250	Jack, Mic (7P)
008G	1	4723120050	Insulator		J806	1	YJ10001620	Jack, Antenna
009G	1	200C005010	Clamper					
010G	1	51100204A0	B.H.M. Screw	B2 x 4				
011G	1	200C056030	Buffer					
012G	1	200C271010	Holder, Lamp					
0000	-	2020110210	Change					
038G	1	203C118010 200C056040	Spacer Buffer					
045G 047G	2	200C056040	Buffer					
047G 050G	1	200C058030	Cover					21
050G	1	53112603A0	Hexagon Nut					
052G	1	62031650W0						



# ALIGNMENT PROCEDURE

## CONDITIONS

- \* All adjustments have been completed prior to shipment. Further adjustments should be limited to a necessary minimum.
- Make sure that all measuring instruments required for alignment are completely calibrated and operate normally.
- \* Before starting measurement, idle the instruments for half-an-hour.

## **Required Measuring Instruments**

- 1. UHF standard signal generator
- 2. RF power meter
- 3. Audio signal generator
- 4. AC/DC voltmeter (VTVM)
- 5. RF voltmeter
- 6. Frequency counter
- 7. Oscilloscope
- 8. Galvanometer
- 9. Regulated DC power supply
- 10. DC ammeter
- 11. (Spectrum analyzer)
- 12. (Digital voltmeter)

## Required Alignment Tools

- Philips screwdriver ... for casing and boards
   Standard screwdriver.. for trimmer resistor and IF adjustment
   Non-metallic standard screwdriver ..... for RF and trimmer capacitor adjustment
   Box screwdriver .... for support (2.6, 2.0mm)
- 3.0mm) For RF circuit and frequency adjustment, use a

non-metallic screwdriver.

# C78 ALIGNMENT PROCEDURE

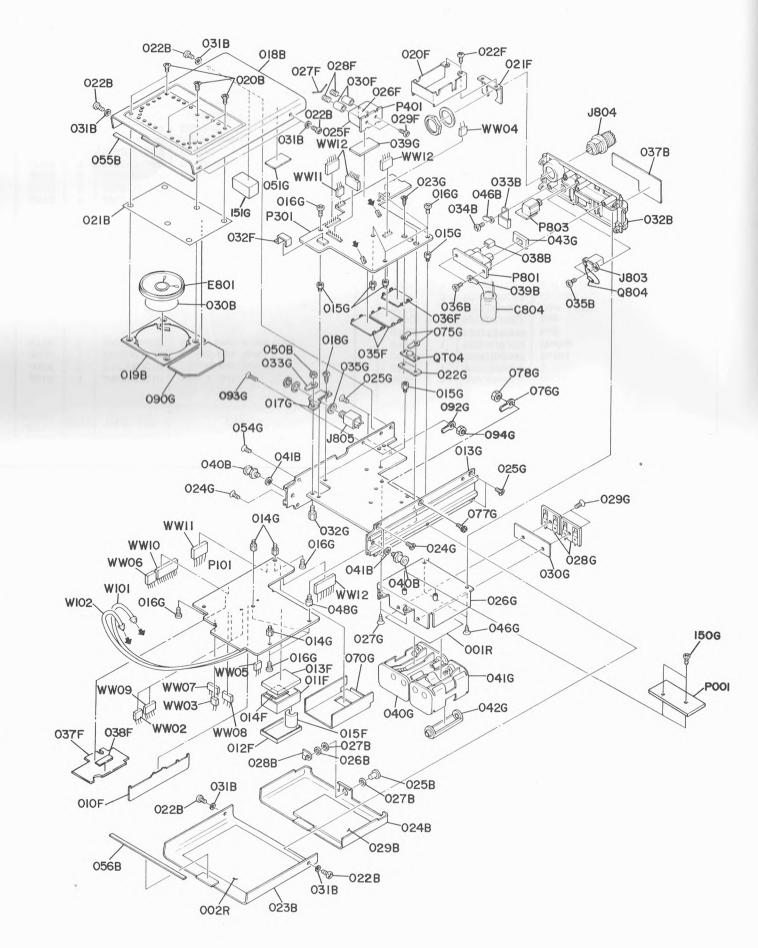
# 1. Standard Alignment Conditions

Supply voltage:	13.8V DC
Audio output:	0.7 watts
Audio output loading:	8.0Ω
Frequency deviation:	±3.5kHz
Modulation frequency:	1kHz
Transmitter load:	50Ω
Reception frequency:	435.00MHz
Transmission frequency: .	435.20MHz

# 2. Alignment Notes

Handle all trimmer resistor gently. Unless otherwise s switches and controls as follow	pecified, set the
1. SQL:	Minimum
2. VOL:	Minimum or most
	adequate level
3. SCAN MODE switch:	FREE
4. RPT switch:	S
5. CHANNEL STEP switch:	
	50kHz
6. Supply voltage:	13.8V DC
7. Frequency:	RX: 435.00MHz

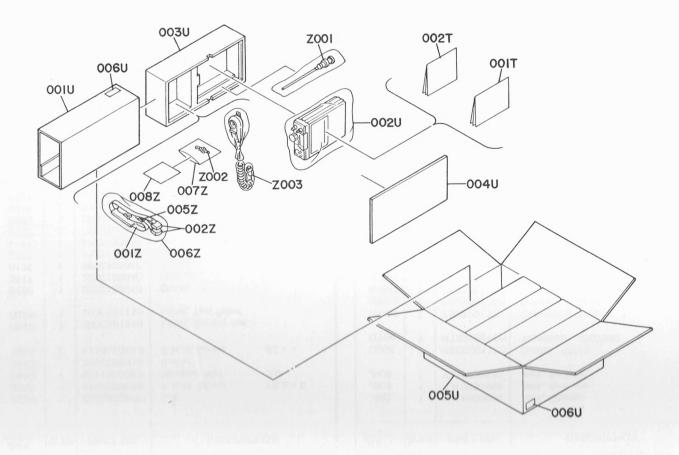
TX: 435.20MHz



C

REF. DESIG.	<b>Ο΄</b> ΤΥ	<b>Ο΄ΤΥ</b>	<b>Ο΄ΤΥ</b>	<b>Δ΄</b> ΤΥ	<b>Ω</b> ΎΤΥ	PART NO.	DESCRIPTION		REF. DESIG.	<b>Δ΄</b> ΤΥ	PART NO.	DESCRIPTION	
018B	1	203C257010	Lid, Upper Case		015G	5	200C101020	Support					
019B	1	200C160010	Bracket, Speaker		016G	10	51102604A0	B.H.M. Screw	B2.6 x 4				
020B	6	51142606S0	O.C.H.M. Screw	2.6 x 6	017G	1	200C160020	Bracket, EXT Speak					
021B	1	200C202010	Net, Speaker	2.0 × 0	018G	1	51400305P0	B.H. Tapped Screw	B3 x 5				
022B	6	51402604T0	B.H. Tapped Screw	B2.6 x 4	022G	1	200C267010	Heatsink					
023B	1	200C257020	Lid, Bottom Case	52.0 X 4	023G	2	51382608P0	P.H. Tapped Screw	P2.6 x 8				
024B	1	200C257030	Lid, Battery Case		024G	3	51042604A0	F.H.M. Screw	F2.6 x 4				
025B	1	200C102030	Lock		025G	4	51042604A0	F.H.M. Screw	F2.6 x 4				
026B	1	200C055010	Collar										
027B	2	59069505G9	Washer		026G	1	200C064310	Case, Battery Tray					
	_				027G	2	51342605P0	F.H. Tapped Screw	F2.6 x 5				
028B	1	200C102040	Lock		028G	2	200C123110	Contactor					
029B	1	200C056020	Buffer		029G	2	51342605P0	F.H. Tapped Screw	F2.6 x 5				
030B	1	200C120060	Insulator, Speaker		030G	1	200C120040	Insulator					
031B	6	54012600A0	Washer		032G	1	200C101030	Support					
032B	1	200C064020	Case, Rear		033G	1	62030039W0	Lug, Ear Jack					
033B	1	200C005020	Clamper		035G	1	200C118030	Spacer					
034B	1	51102604E0	B.H.M. Screw	B2.6 x 4	039G	1	203C118020	Spacer					
035B	2	51100204E0	B.H.M. Screw	B2 x 4	040G	1	200C064040	Case, Battery (6)					
036B	2	51102604E0	B.H.M. Screw	B2.6 x 4									
037B	1	200C265310	Indicator		041G	1	200C064050	Case, Battery (4)					
					042G	1	200C121010	Link, Dummy					
038B	1	200C270010	Button		043G	1	200C118020	Spacer					
039B	1	62261240W0	Lug		046G	2	51342605P0	F.H. Tapped Screw	F2.6 x 5				
040B	2	200C155010	Hanger		048G	1	203C101010	Support					
041B	2	54040402B0	Spring Washer		051G	1	200C056050	Buffer					
					054G	1	51042606A0	F.H.M. Screw	F2.6 x 6				
046B	1	62261240W0	Lug		070G	1	200C053120	Cover					
050B	1	53110303E9	Hexagon Nut		075G	2	62261240W0	Lug					
055B	1	200C118040	Spacer		076G	1	1210005010	Lug					
056B	1	200C118040	Spacer										
013G	1	200C105110	Chassis, H		077G	1	51042604A0	F.H.M. Screw	F 2.6 x 4				
014G	3	200C101010	Support		078G	1	53112603E0	Hexagon Nut 2.6					
					090G	1	200C053040	Buffer					

REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIPTION	REF. DESIG.	Q'TY	PART NO.	DESCRIPTION
092G	1	62030039W0	Lug	J803	1	YJ04000620	Jack, Charger
093G	1	51042605A0	F.H.M. Screw F2.6 × 5	J804	1	YJ10000550	Jack, Antenna
094G	1	53112603E0	Hexason Nut 2.6	J805	1	YJ01000570	Jack, EXT SP.
151G		203C056010	Buffer	3005			
150G	1	51100205A0	B.H.M. Screw B2 x 5	Q804	1	HD20001100	Diode 10D-2
1005		5110020040	D.T.M. OCICW D2X0	QT04	1	HT32283100	Transistor 2SC2283
001R	1	200C861110	Label, Battery Ind.				
002R	1	203C861110	Label, Test Point	WW02	1	YB01001170	Connective Cord
		2000001110	Labor, rost ronnt	WW03	1	YB01001180	Connective Cord
010F	1	203C109010	Shield	WW04	1	YB01001190	Connective Cord
011F	1	203C109040	Shield	WW05	1	YB01001202	Connective Cord
012F	1	203C109060	Shield	WW06	1	YB01001210	Connective Cord
013F	1	4723109240	Shield	WW07	1	YB01001220	Connective Cord
014F	1	4723120020	Insulator	WW08	1	YB01001230	Connective Cord
015F	1	1143259010	Bushing	WW09	1	YB01001240	Connective Cord
020F	1	203C109020	Shield	WW10	1	YB01001250	Connective Cord
021F	1	203C109030	Shield	WW11	1	YB01001260	Connective Cord
022F	2	51282605B0	B.H. Tapped Screw B2.6 x 5	WW12	1	YB01001272	Connective Cord
025F	1	4724109120	Shield				
026F	1	4724109130	Shield	W101	1	YB01001300	Connective Cord
027F	1	4724115010	Spring	W102	1	YB01001310	Connective Cord
028F	2	4724161020	Ferric Core				
029F	2	51282606B0	B.H. Tapped Screw B2.6 x 6				
030F	2	4724275010	Bobin				
032F	1	200C267030	Heatsink				
035F	2	203C109070	Shield				
036F	1	203C109080	Shield				
037F	1	203C109090	Shield				
038F	1	4295120060	Insulator				
E801	1	QK00578010	Speaker, 57mm				
C804	1	EA10802530	Elect Cap. 1000µF 25V				
C804		EA10802530	стест сар. 1000μг 25V				



REF. DESIG.	<b>Ο΄</b> ΤΥ	PART NO.	DESCRIPTION	REF. DESIG.	<b>Ο΄</b> ΤΥ	PART NO.	DESCRIPTION
001T 002T 001U 002U 003U 004U 005U 006U	1 1 1 1 1 3	203C851010 203C856020 9012035010 200C809010 200C803010 203C505020 9523019010	Instructions Circuit Diagram Packing Case Polyethylene Bag Cushion Partitioner Master Carton Serial No. Card	0012 0052 0062 0072 0082 2001 2002 2003	1 2 1 1 1 1 1 1 1	4223156010 200C155500 9011020010 9010510010 200C851130 YR01010020 YP01000310 MP11000690	Strap Hanger, (K) Hanger, Mic Polyethylene Bag Instructions Whip Antenna Plug, Non Short Microphone

REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIPTION	REF. DESIG.	<b>Ω'</b> ΤΥ	PART NO.	DESCRIPTION
001T	1	203C851010	Instructions	001Z	1	4222150010	
002T	1	203C856020	Circuit Diagram	0022	2	4223156010 200C155500	Strap
			U U	005Z	1	200C155020	Hanger, (K) Hanger, Mic
001U	1	203C804020	Packing Case	006Z	i	9011020010	Polyethylene Bag
002U	1	9012035010	Polyethylene Bag	007Z	1	9010510010	Polyethylene Bag
003U	1	200C809010	Cushion	008Z	1	200C851130	Instructions
004U	1	200C803010	Partitioner				manuccions
005U	1	203C505020	Master Carton	Z001	1	YR01010020	Whip Antenna
006U	3	9523019010	Serial No. Card	Z002	1	YP01000310	Plug, Non Short
				Z003	1	MP11000690	Microphone
			*				

REF. DESIG.	Q'TY	PART NO.	DESCRIPTION	REF. DESIG.	<b>Ο΄</b> ΤΥ	PART NO.	DESCRIP	TION	
			P101-PLL CIRCUIT BOARD	CS11	1	DK16102300	Ceramic 0.001µF	±10%	
P101	1	YH203C1310	P.W. Board, PLL	CS12	1	EJ10601610	Elect 10µF		16V
				CS13	1	DK16102300	Ceramic 0.001µF	±10%	
			P101-CAPACITORS	CS14	1	EA47601630	Elect 47 µF		16V
CL01	1	DS17153010	Semicon 0.015µF ±20%	C101	1	DD10050300	Ceramic 5pF	±0.25p	F
CL02	1	DS17153010	Semicon 0.015µF ±20%	C102	1	DD10030300	Ceramic 3pF	±0.25p	F
CL03	1	DS17182010	Semicon 8200pF ±20%	C103	1	DK16102300	Ceramic 0.001µF	±10%	
CL04	1	DF16102300	Film 0.001µF ±10%	C104	1	EJ10601610	Elect 10µF		16V
CL05	1	EV47403560	Elect. 0.47µF 35V	C105	1	DD15151370	Ceramic 150pF	±5%	
CL06	1	DD15220370	Ceramic 22pF ±5%	C106	1	DD15151370	Ceramic 150pF	±5%	
CL07	1	DD15220370	Ceramic 22pF ±5%	C107	. 1	DD15101350	Ceramic 100pF	±5%	
CL08	1	DD15220370	Ceramic 22pF ±5%	C108	1	DD15101350	Ceramic 100pF	±5%	
CL09	1	DD15220370	Ceramic 22pF ±5%	C109	1	DK18103310	Ceramic 0.01µF		
CL11	1	EA22601630	Elect 22µF 16V	C110	1	DK16122300	Ceramic 1200pF	±10%	
CL12	1	DK18103030	Ceramic 0.01µF						
CL13	1	DK16102300	Ceramic 0.001 µF ±10%	C111	1	DK16102300	Ceramic 0.001µF	±10%	
CL14	1	DK16102300	Ceramic 0.001 µF ±10%	C112	1	EV10601660	Elect 10µF		16V
CL15	1	DK16102300	Ceramic 0.001µF ±10%	C113	1	DD15390300	Ceramic 39pF	±5%	
CL16	1	DK16102300	Ceramic 0.001µF ±10%	C114	1	DD15150300	Ceramic 15pF	±5%	
CL17	1	DK16102300	Ceramic 0.001µF ±10%	C115	1	CT12000090	Trimming 20pF		
CL18	1	DK16102300	Ceramic 0.001µF ±10%	C116	1	EV22601060	Elect 22µF		10V
CL20	1	DD10050300	Ceramic 5pF ±0.025pF	C117	1	EV47501060	Elect 4.7µF		10V
CL21	1	DD10050300	Ceramic 5pF ±0.025pF	C118	1	EV10403560	Elect 0.1µF		35V
CS01	1	DK16102300	Ceramic 0.001µF ±10%	C119	1	DK16102300	Ceramic 0.001µF	±10%	
CS02	1	DK16102300	Ceramic 0.001µF ±10%	C120	1	DK16102300	Ceramic 0.001µF	±10%	
CS03	1	DK16102300	Ceramic 0.001µF ±10%			in the second			
CS04	1.	DK16102300	Ceramic 0.001µF ±10%	C121	1	EV22601660	Elect 22µF		16V
CS05	1	DK16102300	Ceramic 0.001µF ±10%	C122	1	DK18102030	Ceramic 0.001µF		
CS06	1	DK16102300	Ceramic 0.001µF ±10%	C123	1	EV47403560	Elect 0.47µF		35V
CS07	1	DK16102300	Ceramic 0.001µF ±10%	C124	1	DK16102300	Ceramic 0.001µF	±10%	
CS08	1	DK16102300	Ceramic 0.001µF ±10%	C125	1	DK16102300	Ceramic 0.001µF	±10%	
CS09	1	DK16102300	Ceramic 0.001µF ±10%						
CS10	1	DK16102300	Ceramic 0.001µF ±10%	CS15	1	DK16102300		±10%	
				CS16	1	EV47501060	Elect. $4.7\mu F$		10V

ELECTRICAL PARTS

DD10050300 DK16102300 DD15430300 DK16102300 CT10600090 DK17102010 DD1050300 DK16102300 DD15240300 DD15200300 DK16102300 DC15470370 DK16102300	Ceramic 5pF Ceramic 0.001µF Ceramic 43pF Ceramic 0.001µF Trimming 6pF Ceramic 0.001µF Ceramic 5pF Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±0.25pF ±10% ±5% ±10% ±20% ±0.25pF ±10% 50∨ ±5% ±5% ±10%	C310 C311 C312 C315 C316 C317 C318 C319 C320 C321 C322	1 1 1 1 1 1 1 1 1	CT11000020 DD10040300 CT11000020 DK18103310 DK18103310 DD15101050 DD15620010 DK18103310 DK18103310	Trimming10pFCeramic4pFTrimming10pFCeramic0.01µFCeramic0.01µFCeramic100pFCeramic62pFCeramic0.01µF	±0.25pF ±5% ±5%
DK16102300 DD15430300 DK16102300 CT10600090 DK17102010 DD1050300 EJ10405010 DD15240300 DK16102300 DK16102300 DD15470370	Ceramic 0.001µF Ceramic 43pF Ceramic 0.001µF Trimming 6pF Ceramic 0.001µF Ceramic 5pF Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±10% ±5% ±10% ±0.25pF ±10% 50V ±5%	C311 C312 C315 C316 C317 C318 C319 C320 C321	1 1 1 1 1 1 1 1	DD10040300 CT11000020 DK18103310 DD15101050 DD15620010 DK18103310 DK18103310	Ceramic4pFTrimming10pFCeramic0.01µFCeramic0.01µFCeramic100pFCeramic62pFCeramic0.01µF	±5%
DD15430300 DK16102300 CT10600090 DK17102010 DD1050300 EJ10405010 DD15240300 DK16102300 DK16102300 DD15470370	Ceramic 43pF Ceramic 0.001µF Trimming 6pF Ceramic 0.001µF Ceramic 5pF Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±5% ±10% ±0.25pF ±10% 50∨ ±5%	C312 C315 C316 C317 C318 C319 C320 C321	1 1 1 1 1 1 1	CT11000020 DK18103310 DK18103310 DD15101050 DD15620010 DK18103310 DK18103310	Trimming 10pF Ceramic 0.01µF Ceramic 0.01µF Ceramic 100pF Ceramic 62pF Ceramic 0.01µF	±5%
DK16102300 CT10600090 DK17102010 DD10050300 EJ10405010 DD15240300 DL15200300 DK16102300 DD15470370	Ceramic 0.001µF Trimming 6pF Ceramic 0.001µF Ceramic 5pF Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±10% ±20% ±0.25pF ±10% 50V ±5%	C315 C316 C317 C318 C319 C320 C321	1 1 1 1 1	DK18103310 DK18103310 DD15101050 DD15620010 DK18103310 DK18103310	Ceramic0.01µFCeramic0.01µFCeramic100pFCeramic62pFCeramic0.01µF	
CT10600090 DK17102010 DD10050300 EJ10405010 DD15240300 DD15200300 DK16102300 DD15470370	Trimming 6pF Ceramic 0.001µF Ceramic 5pF Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±20% ±0.25pF ±10% 50∨ ±5% ±5%	C315 C316 C317 C318 C319 C320 C321	1 1 1 1 1	DK18103310 DK18103310 DD15101050 DD15620010 DK18103310 DK18103310	Ceramic0.01µFCeramic0.01µFCeramic100pFCeramic62pFCeramic0.01µF	
DK17102010 DD10050300 DK16102300 EJ10405010 DD15240300 DK16102300 DD15470370	Ceramic 0.001µF Ceramic 5pF Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±0.25pF ±10% 50∨ ±5% ±5%	C316 C317 C318 C319 C320 C321	1 1 1 1	DK18103310 DD15101050 DD15620010 DK18103310 DK18103310	Ceramic0.01µFCeramic100pFCeramic62pFCeramic0.01µF	
DD10050300 DK16102300 EJ10405010 DD15240300 DD15200300 DK16102300 DD15470370	Ceramic 5pF Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±0.25pF ±10% 50∨ ±5% ±5%	C317 C318 C319 C320 C321	1 1 1 1	DD15101050 DD15620010 DK18103310 DK18103310	Ceramic 100pF Ceramic 62pF Ceramic 0.01µF	
DK16102300 EJ10405010 DD15240300 DD15200300 DK16102300 DD15470370	Ceramic 0.001µF Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±10% 50∨ ±5% ±5%	C318 C319 C320 C321	1 1 1	DD15620010 DK18103310 DK18103310	Ceramic 62pF Ceramic 0.01µF	
EJ10405010 DD15240300 DD15200300 DK16102300 DD15470370	Elect 0.1µF Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	50∨ ±5% ±5%	C319 C320 C321	1 1	DK18103310 DK18103310	Ceramic 0.01µF	±5%
DD15240300 DD15200300 DK16102300 DD15470370	Ceramic 24pF Ceramic 20pF Ceramic 0.001µF	±5% ±5%	C320 C321	1	DK18103310		
DD15200300 DK16102300 DD15470370	Ceramic 20pF Ceramic 0.001µF	±5%	C321				
DK16102300 DD15470370	Ceramic 0.001µF				DD10015300	Ceramic 0.01µF	10.05.5
DK16102300 DD15470370	Ceramic 0.001µF			1	EA10602530	Ceramic 1.5pF	±0.25pF
DD15470370			C323	1	DK16102300	Elect 10µF	25V
		±5%	0525		DK10102300	Ceramic 0.001µF	±10%
	Ceramic 47pF Ceramic 0.001µF	±10%	C324	1	DD10020300	o	
DK16102300	Ceramic 0.001µF	±10%	C325	i	DD10020300	Ceramic 2pF	±0.25pF
DK16102300	Ceramic 0.001µF	±10%	C401	i	DK16102300	Ceramic 2pF	±0.25pF
EJ10405010	Elect 0.1µF	±10%	C401	1	EJ22505010	Ceramic 0.001µF	±10%
DD15160300	Ceramic 16pF	±5%	C402	1	EV10403560	Elect 2.2µF	50V
DD15180300	Ceramic 18pF	±5%	C404	1	EJ22505010	Elect 0.1µF	35V
DD15470370	Ceramic 47pF	±5%	C405	1	DK16471300	Elect 2.2µF	50 V
DD10020300	Ceramic 2pF	±0.25pF	C406	1	DK16471300	Ceramic 470pF	±10%
DD10020300	Ceramic 2pF	±0.25pF	C407	1	EV22403560	Ceramic 470pF	±10%
0010020300	Ceranne zpr	10.20pr	C408	1	DF16103300	Elect 0.22µF	35V
DK18502010	Ceramic 0.005µF		0400		DF10103300	Film 0.01µF	±10%
DK18103030	Ceramic 0.01µF		C409	1	DE17922200	<b>F</b> 'I	
DK18103030	Ceramic 0.01µF					ore only it	±20%
DK18103030	Ceramic 0.01µF					a a a b i	±10%
DD10040300	Ceramic 4pF	±0.25pF					±10%
CT11000020	Trimming 10pF						±10%
DD10040300	Ceramic 4pF	±0.25pF					±10% 35V
CT11000020	Trimming 10pF	and the second sec					
DD10040300	Ceramic 4pF	±0.25pF	0.14		5110102000	Ceramic 0.001µP	±10%
DDDDCDC	K18103030 K18103030 K18103030 D10040300 T11000020 D10040300 T11000020	K18103030         Ceramic         0.01µF           K18103030         Ceramic         0.01µF           K18103030         Ceramic         0.01µF           K18103030         Ceramic         0.01µF           D10040300         Ceramic         4pF           T11000020         Trimming         10pF           D10040300         Ceramic         4pF           T11000020         Trimming         10pF	K18103030       Ceramic       0.01µF         K18103030       Ceramic       0.01µF         K18103030       Ceramic       0.01µF         K18103030       Ceramic       0.01µF         D10040300       Ceramic       4pF       ±0.25pF         T11000020       Trimming       10pF         D10040300       Ceramic       4pF       ±0.25pF         T11000020       Trimming       10pF         T11000020       Trimming       10pF	K18103030       Ceramic $0.01\mu$ F       C409         K18103030       Ceramic $0.01\mu$ F       C410         K18103030       Ceramic $0.01\mu$ F       C410         K18103030       Ceramic $0.01\mu$ F       C411         D10040300       Ceramic $4pF$ $\pm 0.25pF$ C412         T11000020       Trimming $10pF$ C413         D10040300       Ceramic $4pF$ $\pm 0.25pF$ C326         T11000020       Trimming $10pF$ C414	K18103030Ceramic $0.01\mu$ FC4091K18103030Ceramic $0.01\mu$ FC4101K18103030Ceramic $0.01\mu$ FC4111D10040300Ceramic4pF $\pm 0.25p$ FC4121T11000020Trimming10pFC4131D10040300Ceramic4pF $\pm 0.25p$ FC3261T11000020Trimming10pFC3261T11000020Trimming10pFC4141	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

REF. DESIG.	<b>Ο΄</b> ΤΥ	PART NO.	DES	CRIPTION	REF. DESIG.	<b>Δ΄ΤΥ</b>	PART NO.	DESCRIPTION
			P101-RESIST	DBS	R101	1	GD05104140	100KΩ
				re ±5% and ¼W)	R102	1	GD05222140	2.2ΚΩ
RL01	1	GD05103180	10KΩ	1/8W	R103	1	GD05101140	100Ω
RL02	1	GD05103180	10KΩ	1/8W	R104	1	GD05224140	220KΩ
RL03	1	GD05822180	8.2KΩ	1/8W	R105	1	GD05332140	3.3KΩ
RL04	1	GD05103180	10KΩ	1/8W	R106	1	GD05823140	82K Ω
RL05	1	GD05103140	10KΩ		R107	1	GD05681140	680 Ω
RL06	1	RA01020330	1KΩ(B)	Trimming	R108	1	GD05101140	100Ω
RL07	1	GD05272140	2.7KΩ		R109	1	GD05221140	220 Ω
RL08	1	GD05152140	1.5KΩ		R110	1	GD05221140	220Ω
RL09	1	GD05333140	<b>33K</b> Ω					
RL10	1	GD05104140	100KΩ		R111	1	GD05332140	3.3KΩ
					R112	1	GD05822140	8.2KΩ
<b>RL11</b>	1	GD05393140	39KΩ		R113	1	RC00000140	ΩΟ
<b>RL12</b>	1	GD05473140	47KΩ		R114	1	GD05822140	8.2ΚΩ
<b>RL13</b>	1	GD05472140	<b>4.7K</b> Ω		R115	1	GD05272140	2.7ΚΩ
<b>RL14</b>	1	GD05563140	56KΩ		R116	1	RA04720120	4.7KΩ Trimming
<b>RL15</b>	1	GD05563180	56KΩ	1/8W	R117	1	GD05273140	27ΚΩ
<b>RL16</b>	1	GD05563180	56KΩ	1/8W	R118	1	GD05103140	10KΩ
					R119	1	GD05224140	220ΚΩ
RS01	1	GD05333140	33KΩ		R120	1	GD05101140	100Ω
<b>RS02</b>	1	GD05332140	3.3KΩ					
<b>RS03</b>	1	GD05103140	10KΩ		R121	1	GD05182140	1.8KΩ
<b>RS04</b>	1	GD05102140	1KΩ		R122	1	GD05472140	4.7ΚΩ
<b>RS05</b>	1	GD05103140	10KΩ		R123	1	GD05391140	390Ω
<b>RS06</b>	1	GD05682140	6.8KΩ		R124	1	GD05101140	100Ω
<b>RS07</b>	1	GD05392140	3.9KΩ		R125	1	GD05101140	100Ω
<b>RS08</b>	1	GD05223140	22KΩ					
RS09	1	GD05474140	470KΩ		R127	1	GD05563140	56KΩ
<b>RS10</b>	1	GD05103140	10KΩ		R128	1	GD05103140	10ΚΩ
					R129	1	GD05102140	1ΚΩ
<b>RS11</b>	1	GD05103140	10KΩ		R130	1	GD05470140	47Ω
<b>RS12</b>	1	GD05102140	1KΩ					
<b>RS13</b>	1	GD05333140	33KΩ					

REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	1	DESCRIPTION	REF. DESIG.	<b>Δ΄</b> ΤΥ	PART NO.	DESCRIPTION
R131	1	GD05101140	100Ω		GL01	1	BW10563020	56K $\Omega$ Resistor Compo.
R132	1	GD05681140	680Ω		GL02	1	BW10563020	$56K\Omega$ Resistor Compo.
R133	1	GD05473140	47KΩ		GL02	1	BW10563020	$56K\Omega$ Resistor Compo.
R134	1	RC00000140	0Ω		G101	1	BW10823010	$82K\Omega$
R135	1	GD05027180	2.7 \		G102	1	BW10393010	39KΩ
R203	1	GD05222140	2.2KΩ				51110000010	551(12
R301	1	GD05182140	1.8KΩ					P101-SEMICONDUCTORS
R302	1	GD05182140	1.8KΩ		QL01	1	HC10041060	IC µPD650C
R303	1	GD05182140	1.8KΩ		QL02	1	HC10014170	IC MC14011BCP
R305	1	GD05222140	2.2KΩ		QL03	1	HC10012170	IC MC14016BCP
R306	1	GD05222140	2.2KΩ		QL04	1	HD20011050	Diode 1S1555
R307	1	GD05152140	1.5KΩ		QL05	1	HD20011050	Diode 1S1555
R308	1	GD05471140	470Ω		QL06	1	HD20011050	Diode 1S1555
R309	1	GD05182140	1.8KΩ		QL07	1	HD20011050	Diode 1S1555
R310	1	GD05151140	150Ω		QL08	1	HD20011050	Diode 1S1555
R401	1	RA01020330	1KΩ	Trimming	QL09	1	HD20011050	Diode 1S1555
R402	1	GD05152140	1.5KΩ		QL10	1	HT305360F0	Transistor 2SC536F
R403	1	GD05472140	4.7KΩ		QL11	1	HD20011050	Diode 1S1555
R404	1	GD05472140	4.7KΩ		QL12	1	HD20011050	Diode 1S1555
R405	1	GD05334140	330K Ω					
R406	1	GD05472140	4.7KΩ		QS02	1	HT309451Q0	Transistor 2SC945(Q)
					QS03	1	HD30077090	Zener XZ062
R407	1	GD05153140	<b>15K</b> Ω		QS04	1	HD20011050	Diode 1S1555
R408	1	GD05104140	100KΩ		Q\$05	1	HD20011050	Diode 1S1555
R409	1	GD05105140	1MΩ		Q\$06	1	HC10022060	IC μPC78L08
R410	1	GD05123140	12KΩ		Q\$07	1	HT107381B0	Transistor 2SA738(B)
R411	1	GD05123140	12KΩ		Q\$08	1	HT107381B0	Transistor 2SA738(B)
R412	1	GD05333140	33KΩ		Q.\$09	1	HT309451Q0	Transistor 2SC945(Q)
R413	1	GD05822140	8.2KΩ		QS10	1	HT312131B0	Transistor 2SC1213(B)
R414	1	GD05822140	8.2KΩ		QS11	1	HD30078090	Zener XZ076
R415	1	GD05822140	8.2KΩ					
R416	1	RA01020330		Trimming				
R417	1	GD05473140	47KΩ					
R418	1	GD05103180	10KΩ	1/8W				

REF. DESIG.	<b>Ω'</b> ΤΥ	PART NO.	DESCRIPTION	REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIPTION
Q\$12	1	HT313681B0	Transistor 2SC1368(B)	Q118	1	HT32644000	Transistor 2SC2644
QS12	1	HD30060090	Zener XZ090	Q119	1	HT305351B0	Transistor 2SC535(B)
QS14	1	HD20011050	Diode 1S1555	Q120	1	HT32644000	Transistor 2SC2644
QS15	1	HT107381B0	Transistor 2SA738(B)	Q121	1	HD20011050	Diode IS1555
QS16	1	HT309451Q0	Transistor 2SC945(Q)	Q122	1	HD20011050	Diode IS1555
QS17	1	HD30033090	Zener WZ052	0123	1	HD20011050	Diode IS1555
QS18	1	HD20011050	Diode 1S1555	Q124	1	HD20011050	Diode IS1555
QS19	1	HT309451Q0	Transistor 2SC945(Q)	Q301	1	HD20011050	Diode 1S1555
Q\$20	1	HD30078090	Zener XZ076	Q302	1	HD20011050	Diode 1S1555
QS21	1	HT309451Q0	Transistor 2SC945(Q)	Q303	1	HD20011050	Diode 1S1555
2021				Q304	1	HD20011050	Diode 1S1555
Q\$22	1	HD30077090	Zener XZ062	Q306	1	HD300330Q0	Zener WZ052
QS23	1	HD20011050	Diode 1S1555	Q307	1	HD300330Q0	Zener WZ052
QS24	1	HV00002060	Varistor VD1212	0308	1	HT304611B0	Transistor 2SC461(B)
Q\$25	1	HD20011050	Diode IS1555	Q401	1	HC10003090	IC NJM4558D
GOLO				Q402	1	HC10003090	IC NJM4558D
Q101	1	HT305351B0	Transistor 2SC535(B)				
0102	1	HT304601B0	Transistor 2SC460(B)	1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1			P101-MISCELLANEOUS
Q103	1	HT304601B0	Transistor 2SC460(B)	JL01	1	YJ07000490	Jack (9P)
Q104	1	HV00002060	Varistor VD1212	JL02	1	YJ07000440	Jack (4P)
Q105	1	HV00002060	Varistor VD1212	JL03	1	YJ07000440	Jack (4P)
Q106	1	HV00002060	Varistor VD1212	JL04	1	YJ07000450	Jack (5P)
Q107	1	HV00002060	Varistor VD1212	JL05	1	YJ07000430	Jack (3P)
Q108	1	HV00002060	Varistor VD1212	JL06	1	YJ07000460	Jack (6P)
Q109	1	HV00002060	Varistor VD1212	JS01	1	YJ07000500	Jack (10P)
Q110	1	HV00002060	Varistor VD1212	JS02	1	YJ07000460	Jack (6P)
	-			J301	1	YJ07000430	Jack (3P)
Q111	1	HV00002060	Varistor VD1212	J401	1	YJ07000440	Jack (4P)
Q112	1	HV00002060	Varistor VD1212	J402	1	YP10002210	Plug
Q113	1	HC10011170	IC MC145106P	J403	1	YP10002210	Plug
Q114	1	HT309451Q0	Transistor 2SC945(Q)	LL01	1	L170329040	I.F.T. Coil, Control Clock
Q115	1	HT107331R0	Transistor 2SA733(R)	LS01	1	LC11020070	Choke Coil. $1\mu$ H
Q116	1	HD20011050	Diode 1S1555	LS02	1	LC11040010	Choke Coil, 100µH
Q117	1	HT305351B0	Transistor 2SC535(B)				and the second

REF. DESIG.	<b>Ο'ΤΥ</b>	PART NO.	DESCRIPTION	REF. DESIG.	<b>Ο΄</b> ΤΥ	PART NO.	DESCRIPTION
L101	1	LC14730050	Choke Coil, 47µH				P201-VCO CIRCUIT BOARD
L102	1	LA70280030	Antenna Coil, VCO Buff.	P201	1	YF203C0010	P.W. Board, VCO
L104	1	LC15000140	Choke Coil. 2T			11 20300010	F.W. Board, VCO
L105	1	LC11020070	Choke Coil, 1µH				P201-CAPACITORS
L107	1	LM42518010	Twist Coil, 5T	C201	1	DD10020300	
L108	1	LA70260060	Antenna Coil, Cavity	C202	1	DD15330300	Ceramic 2pF ±0.25pF Ceramic 33pF ±5%
				C203	1	DD10030300	
L301	1	LC12230090	Choke Coil, 22µH	C203	1	DD10030300	et. Tourobi
L302	1	LC11220030	Choke Coil, 1.2µH	C204	1	DK16102300	Ceramic 1pF ±0.25pF
L303	1	LC12230090	Choke Coil, 22µH	C205	1	DD15150300	Ceramic $0.001\mu$ F $\pm 10\%$ Ceramic $15\rho$ F $\pm 5\%$
L304	1	LC11220030	Choke Coil, 1.2µH	C200	1	CT10600090	
L305	1	LC12230090	Choke Coil, 22µH	C207	1	DD15240300	Trimming 6pF
L306	1	LC11220030	Choke Coil, 1.2µH	C208	1	DD15240300	Ceramic 24pF ±5%
L307	1	LC12230090	Choke Coil, 22µH	C205	1	DD11100300	Ceramic 10pF ±0.5pF
L308	1	LC11220030	Choke Coil, 1.2µH	6210		0011100300	Ceramic 10pF ±0.5pF
L311	1	LC13940010	Choke Coil, 390µH	C211	1	DD15150300	0
L312	1	LC13940010	Choke Coil, 390µH	C211			Ceramic 15pF ±5%
L313	1	LC11020070	Choke Coil, 1µH	C212	1	EV33501660	Elect 3.3µF 16
L314	1	LA70280030	Antenna Coil, PLL Local OSC	C213	1	EJ10601610	Elect 10µF 16\
L315	1	LA70280030	Antenna Coil, PLL Local OSC	C251	1	DC18202020	Feedthru 2000pF
L316	1	LC13940010	Choke Coil, 390µH	C252	1	DC18202020	Feedthru 2000pF
				C253	1	DC18202020	Feedthru 2000pF
SL01	1	SS01020340	Slide Switch, CH Step 20K-40K		1	DC18202020	Feedthru 2000pF
SL02	1	SS01020340	Slide Switch, Back Up	C255	1	DC18202020	Feedthru 2000pF
W101	1	YB01001300	Connective Cord				P201-RESISTORS
W102	1	YB01001310	Connective Cord				(All Resistors are ±5% and ¼W)
			Soundarie Cold	R201	1	GD05103140	10ΚΩ
X101	1	XY41024002	Crystal 10.24MHz	R202	1	GD05473140	47ΚΩ
X301	1	XB303007G2	Crystal 40.0825MHz	R204	1	GD05101140	100Ω
X302	1	XB303008G2	Crystal 40.0825MHz Crystal 40.5825MHz	R205	1	GD05104140	100ΚΩ
X303	1	XB303009G2		R206	1	GD05101140	100Ω
X304	1	XB303010G2	Crystal 42.2225MHz	R207	1	GD05104140	100ΚΩ
		1030301002	Crystal 42.7225MHz	R208	1	GD05560140	<b>56</b> Ω

REF. DESIG.	<b>Δ'ΤΥ</b>	PART NO.	DESCRIPTION	REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIP	TION	
				CR01	1	DK16122300	Ceramic 1200pF	±10%	
		and the second second	P201-SEMICONDUCTORS	CR04	1	DD15470300	Ceramic 47pF	±5%	
0201	1	HD40001060	Varicap 1SV50	CR05	1	DK16122300	Ceramic 1200pF	±10%	
0202	1	HD40001060	Varicap 1SV50	CR06	1	DK18103310	Ceramic 0.01µF		
0203	1	HD40001060	Varicap 1SV50	CR07	1	CT10600090	Trimming 6pF		
0204	1	HT200191B0	F.E.T. 2SK19-GR	CR08	1	DK18103310	Ceramic 0.01µF		
0205	1	HT305351C0	Transistor 2SC535(C)	CR09	1	DS17223010	Semicon 0.022µF	±20%	
				CR10	1	DS17223010	Semicon 0.022µF	±20%	
			P201-MISCELLANEOUS	CR11	1	DS17223010	Semicon 0.022µF	±20%	
L201	1	LC12720080	Choke Coil, 2.7µH	CR12	1 1	DD15101350	Ceramic 100pF	±5%	
L202	1	LC12720080	Choke Coil, 2.7µH	0		0010101000			
L203	1	LA70350010	Antenna Coil, VCO	CR13	1	DK18103310	Ceramic 0.01µF		
L204	1	LC12720080	Choke Coil, 2.7µH	CR14	1	EV33601060	Elect 33µF		10\
				CR15	1	DD15360300	Ceramic 36pF	±5%	
			and the second second second second	CR16	1	DD15560330	Ceramic 56pF	±5%	
			P301-TX. RX. CIRCUIT BOARD	CR17	1	DS17223010	Semicon 0.022µF	±20%	
P301	1	YH203C2310	P.W. Board, TX. RX	CR18	1 1	EJ10405010	Elect 0.1µF		50\
				CR19	1	DD15430330	Ceramic 43pF	±5%	
			P301-CAPACITORS	CR20	1	DD15120300	Ceramic 12pF	±5%	
CF01	1	DD15101370	Ceramic 100pF ±5%	CR21	1	DK18103310	Ceramic 0.01µF		
CF02	1	DK18102030	Ceramic 0.001µF	CR22	1	DS17152010	Semicon 1500pF	±20%	
CF03	1	DD10030310	Ceramic 3pF ±0.25pF	CITZZ		Donnozono			
CF04	1	DK46102300	Ceramic 0.001µF ±10%	CR23	1	EV10503560	Elect 1µF		35\
CF05	1	DD11100300	Ceramic 10pF ±0.25pF	CR24	1	EV10502560	Elect 1µF		25
CF06	1	DD10050300	Ceramic 5pF ±0.25pF	CR25	1	DF16153300	Film 0.015µF	±10%	
CF07	1	DD10050300	Ceramic 5pF ±0.25pF	CR26	1	DS17473010	Semicon 0.047µF	±20%	
CF08	-1	DD15470370	Ceramic 47pF ±5%	CR27	1	DS17332010	Semicon 3300pF	±20%	
CF09	1	DK18102030	Ceramic 0.001µF	CR28	1	DK16471300	Ceramic 470pF		
CF10	1	DK18102030	Ceramic 0.001µF	CR29	1	EJ10505010	Elect 1µF		50
CF11	1	DD45470300	Ceramic 470pF ±5%	CR30	1	DK16102300	Ceramic 0.001µF		
				CR31	1	DS17223010	Semicon 0.022µF	±20%	
				CR32		DS17222010	Semicon 2200pF	±20%	

REF. DESIG.	<b>Ω΄</b> ΤΥ	PART NO.	DESCRI	IPTION		REF. DESIG.	<b>Δ΄</b> ΤΥ	PART NO.	DESCRI	TION
CR33	1	DS17223010	Semicon 0.022µF	±20%		СТ06	1	DK19102020	0	
CR34	1	DS17223010	Semicon 0.022µF			CT07	1	DK18102030 DD15470370	Ceramic 0.001µF	100
CR35	1	DS17473010	Semicon 0.022µF			CT08		DK18102030	Ceramic 47pF	±5%
CR36	1	DS17473010	Semicon 0.047µF			СТ09	1	CT11000020	Ceramic 0.001µF	
CR37	1	EJ10603510	Elect 10µF		35V	CT10	1	DK18102030	Trimming 10pF	
CR38	1	DS17223010	Semicon 0.022µF		550	CT11	1	DD15470370	Ceramic 0.001µF Ceramic 47pF	+===
CR39	1	DF16223300	Film 0.022µF			CT12	1			±5%
CR40	1	EV10502560	Elect $1\mu F$		25V	CT12	1	DK18102030 DD15470370	Ceramic 0.001µF	1.50
CR41	1	EA47601030	Elect 47µF		IOV	CT14	1		Ceramic 47pF	±5%
CR42	1	DF16103300	Film 0.01µF			CT15	1	DK18102030 CT11000020	Ceramic 0.001µF	
200.07			0.01/21	-10%		CT16			Trimming 10pF	
CR43	1	EA47601630	Elect 47µF	-	6V	CT17	1	DK18102030	Ceramic 0.001µF	Imai
CR44	1	DF16103300	Film 0.01µF			CT18		DD15470370	Ceramic 47pF	±5%
CR45	1	DK16331300	Ceramic 330pF			CT19	1	EA22601630 DK18102030	Elect 22µF	16
<b>CR46</b>	1	EV10403560	Elect 0.1µF		5V	CT20	1		Ceramic 0.001µF	150
CR47	1	EA22701030	Elect 220µF		ov	CT21	1	DD15470370	Ceramic 47pF	±5%
CR48	1	EV10403560	Elect 0.1µF		5V	CT22	1	DD10020300 CT11000020	Ceramic 2pF	±0.25pF
CR49	1	EA22701630	Elect 220µF		6V	CT23	1	CT11000020	Trimming 10pF	
CR50	1	EA22601630	Elect 22µF		6V	CT24	1	DD11070300	Trimming 10pF Ceramic 7pF	
CR51	1	EJ47502510	Elect 4.7µF		5V	CT25	1	DK18102030	Ceramic 7pF Ceramic 0.001µF	±0.5pF
CR52	1	EV10403560	Elect 0.1µF		5V	CT26	1	DD15470370		150
CR53	1	EV10403560	Elect 0.1µF		5V	CT27	1	DK18102030	Ceramic 47pF Ceramic 0.001µF	±5%
CR54	1	DK18103310	Ceramic 0.01µF			CT28	1	EA22602530		
CR55	1	DD10020300	Ceramic 2pF	±0.25pF		CT29	1	DD10020300		25
CR56	1	DF16104010	Film 0.1µF	±10%		CT30	1	CT11000020		±0.25pF
CR57		DD15470300	Ceramic 47pF	±5%		0.00		C111000020	Trimming 10pF	
CR58		DD10020300	Ceramic 2pF	±0.25pF		CT31	1	DD11070300	Ceramic 7pF	
CT01	1	DD15200300	Ceramic 20pF	±5%		CT32	1	CT11000020		±0.5pF
CT02		DK18102030	Ceramic 0.001µF	-070		CT33		DD10050300	0	+0.05-5
СТОЗ		DD15470370	Ceramic 47pF	±5%		CT34		DK18102030	Ceramic 5pF Ceramic 0.001µF	±0.25pF
СТ04		DD15470370	Ceramic 47pF	±5%		CT35	1	DK18102030	Ceramic $0.001\mu$ F	
СТ05	1	DK18102030	Ceramic 0.001µF	_0/0		CT36		DK18102030	Ceramic 0.001µF	
								2		

REF. DESIG.	<b>Δ΄</b> ΤΥ	PART NO.	DESCRIPTION	REF. DESIG.	Q'TY	PART NO.	DESCRIPTION
СТ37	1	EA22601630	Elect 22µF 16V	RR13	1	GD05101140	100Ω
CT38	1	DK18102030	Ceramic 0.001µF	<b>RR15</b>	1	GD05222140	2.2ΚΩ
CT39	1	DK18102030	Ceramic 0.001µF	RR16	1	GD05222140	2.2KΩ
CT40	1	DD15470370	Ceramic 47pF	RR17	1	GD05473140	47ΚΩ
CT41	1	DK18102030	Ceramic 0.001µF	RR18	1	GD05123140	12ΚΩ
CT42	1	DD15470370	Ceramic 47pF ±5%	RR19	1	GD05103140	10ΚΩ
CT43	1	DM15001510	Minic 0.15pF ±5%	RR20	1	GD05103140	10KΩ
CT44	1	DK18102030	Ceramic 0.001µF	RR21	1	GD05103140	10KΩ
CT45	1	DK18102030	Ceramic 0.001µF	RR22	1	GD05224140	220ΚΩ
CT60	1	DD15470300	Ceramic 47pF ±5%				
				RR23	1	GD05392140	3.9KΩ
CT46	1	DK16102300	Ceramic 0.001µF ±10%	RR24	1	GD05562140	5.6KΩ
CT47	1	DK16102300	Ceramic 0.001µF ±10%	RR25	1	GD05103140	10KΩ
CT48	1	DK16102300	Ceramic 0.001µF ±10%	RR26	1	GD05823140	82KΩ
CT49	1	DK16102300	Ceramic 0.001µF ±10%	RR27	1	GD05222140	2.2KΩ
CT50	1	DD10020300	Ceramic 2pF ±0.25pF	RR28	1	GD05104140	100KΩ
CT61	1	DD15470300	Ceramic 47pF ±5%	RR29	1	GD05334140	330KΩ
				RR30	1	GD05471140	470Ω
			P301-RESISTORS	RR31	1	GD05222140	2.2KΩ
			(All Resistors are ±5% and ¼W)	RR32	1	GD05471140	470Ω
<b>RF01</b>	1	GD05272140	2.7ΚΩ				
<b>RF02</b>	1	RA02230090	22KΩ Trimming, VR	RR33	1	RA02230090	22KΩ Trimming
<b>RF03</b>	1	GD05221140	220Ω	RR34	1	GD05333140	33KΩ
				RR35	1	GD05273140	27ΚΩ
<b>RR01</b>	1	GD05153140	15ΚΩ	RR36	1	GD05333140	33KΩ
<b>RR02</b>	+1	GD05103140	10KΩ	RR37	1	GD05822140	8.2ΚΩ
<b>RR04</b>	1	GD05101140	100Ω	RR38	1	RA04720120	4.7KΩ Trimming
<b>RR05</b>	1	GJ05101010	100Ω 1W	RR39	1	GD05822140	8.2ΚΩ
<b>RR07</b>	1	GD05101140	100Ω 1W	RR40	1	GD05103140	10ΚΩ
<b>RR08</b>	1	GD05331140	330Ω	RR41	1	GD05223140	22ΚΩ
<b>RR09</b>	1	GD05123140	12ΚΩ	RR42	1	GD05223140	22ΚΩ
<b>RR10</b>	1	GD05273140	27ΚΩ				
<b>RR11</b>	1	GD05102140	1ΚΩ				
<b>RR12</b>	1	GD05221140	220Ω				

REF. DESIG.	<b>Ο΄</b> ΤΥ	PART NO.	DESCRIPTION	REF. DESIG.	<b>Δ΄</b> ΤΥ	PART NO.	DESCRIPTION
RR43	1	GD05104140	100KΩ	RT13	1	GD05221140	220Ω
<b>RR44</b>	1	GD05102140	1ΚΩ	RT14	1	GD05821140	8200
<b>RR45</b>	1	GD05391140	390Ω	RT15	1	GD05680140	68Ω
<b>RR46</b>	1	GD05153140	15ΚΩ	RT16	1	GD05101140	100Ω
<b>RR47</b>	1	GD05393140	39KΩ	RT17	1	GD05561140	560 Ω
<b>RR48</b>	1	GD05224140	220ΚΩ	RT18	1	GD05222140	2.2KΩ
<b>RR49</b>	1	GD05103140	10ΚΩ	RT19	1	GD05102140	1ΚΩ
<b>RR50</b>	1	GD05103140	10KΩ	RT20	1	RA04720120	4.7KΩ Trimming
<b>RR51</b>	1	RA02230090	22KΩ Trimming	RT21	1	GD05271140	270Ω
<b>RR52</b>	1	GD05103140	10KΩ	RT22	1	GD05152140	1.5ΚΩ
				RT23	1	GD05102140	1ΚΩ
<b>RR53</b>	1	GD05182140	1.8KΩ	RT24	1	GD05151180	150Ω 1/8W
<b>RR54</b>	1	RC00000140	ΩΩ				10012 1/000
RR55	1	GD05105140	1MΩ				P301-SEMICONDUCTORS
<b>RR56</b>	1	GD05332140	3.3KΩ	QF01	1	HF20001200	Diode MI301
<b>RR57</b>	1	GD05104140	100KΩ	QF02	1	HF20001200	Diode MI301
<b>RR58</b>	1	GD05332140	3.3KΩ	QF03	1	HD10005020	Diode OA99
<b>RR59</b>	1	GD05102140	1ΚΩ			1.01000020	Blode GASS
<b>RR60</b>	1	RC00000140	ΩΟ	QR01	1	HF40048100	F.E.T. 3SK48
<b>RR61</b>	1	GD05103140	10ΚΩ	QR02	1	HF40048100	F.E.T. 3SK48
RR62	1	GD05151140	150Ω	QR03	1	HT304601B0	Transistor 2SC460(B)
RT01	1	GD05682140	6.8KΩ	QR04	1	HT309451Q0	Transistor 2SC945(Q)
RT02	1	GD05152140	1.5ΚΩ	QR05	1	HT309451Q0	Transistor 2SC945(Q)
RT03	1	GD05472140	4.7ΚΩ	QR06	1	HT309001E0	Transistor 2SC900(E)
RT04	1	GD05472140	4.7ΚΩ	QR07	1	HT309451Q0	Transistor 2SC945(Q)
RT05	1	GD05122140	1.2ΚΩ	QR08	1	HT309451Q0	Transistor 2SC945(Q)
RT06	1	GD05332140	3.3KΩ	QR09	1	HT107331Q0	Transistor 2SA733(Q)
RT07	1	GD05821140	820Ω	QR10	1	HC10015170	IC MC3357-P
RT08	1	GD05220140	220Ω				
RT09	1	GD05221140	220Ω	QR11		HC10037060	IC μPC575C2
RT10	1	GD05332140	3.3KΩ	QR12		HC10013170	IC MC14001CP
DT44		ODOFOOL	1.1.1.1	QR20		HD10005020	Diode OA99
RT11		GD05821140	820Ω	QR21	1	HD10005020	Diode OA99
RT12	1	GD05150140	150Ω	QR22	1	HD10005020	Diode OA99

REF. DESIG.	Q'TY	PART NO.	DESCRIPTION
QR23	1	HD10005020	Diode OA99
<b>QR24</b>	1	HD20011050	Diode 1S1555
<b>QR25</b>	1	HD20011050	Diode 1S1555
<b>QR26</b>	1	HD20011050	Diode 1S1555
<b>QR28</b>	1	HD20011050	Diode 1S1555
QR29	1	HD30029090	Zener WZ090
QR30	1	HD20011050	Diode IS1555
QR31	1	HD20011050	Diode IS1555
QR32	1	HD20011050	Diode IS1555
QT01	1	HF40070100	F.E.T. 3SK70
QT02	1	HT32407100	Transistor 2SC2407
QT03	1	HT32644000	Transistor 2SC2644
QT04	1	HT32283100	Transistor 2SC2283
QT05	1	HT106731B0	Transistor 2SA673(B)
QT06	1	HT309451Q0	Transistor 2SC945(Q)
QT07	1	HC10022060	IC μPC78L08
<b>QT08</b>	1	HT107381B0	Transistor 2SA738(B)
QT09	1	HD10005020	Diode 01399
QT11	1	HD20005060	Diode 1SS16
QT12	1	HD20005060	Diode 1SS16
			P301-MISCELLANEOUS
FR01	1	XU421400M5	Crystal 21.4MHz
FR02	1	FG455304E0	Ceramic Filter CFU455B
FR03	1	FG455304E0	Ceramic Filter CFU455B
FT01	1	FC90050010	Ferrite Core
JF02	*1	YJ07000430	Jack (3P)
JR01	1	YJ07000360	Jack
JR02	1	YJ07000430	Jack (3P)
JR03	1	YJ07000470	Jack (7P)
JR04	1	YJ07000460	Jack (6P)
JT01	1	YJ07000360	Jack (4D)
JT02	1	YJ07000440	Jack (4P)

REF. DESIG.	Q'TY	PART NO.	DESC	CRIPTION
LF01	1	LC15000110	Choke Coil,	2T
LF02	1	LC15000110	Choke Coil,	2T
LF03	1	LC15000110	Choke Coil,	2T
LR03	1	LA70260070	Antenna Coil	
LR04	1	LC15000110	Choke Coil,	2T
LR05	1	L170280020	I.F.T. Coil, 21	.4MHz
_R06	1	L170280020	I.F.T. Coil, 21	.4MHz
_R07	1	L170280010	I.F.T. Coil, 21	.4MHz
LR08	1	LI71016090	I.F.T. Coil, 45	5KHz
LR09	1	LC24760010	Choke Coil,4.7	7mH
LT01	1	LM42417010	Twist Coil,	1 - 4
LT02	1	LM42417010	Twist Coil,	1 - 4
LT03	1	LC12010012	Choke Coil,	8T
LT04	1	LM42518010	Twist Coil,	5T
LT05	1	LC12010012	Choke Coil,	8T
LT06	1	LC15000110	Choke Coil,	2T
LT07	1	LM42518010	Twist Coil,	5T
_T08	1	LC12010012	Choke Coil,	8T
_T10	1	LM42518010	Twist Coil,	5T
LT11	1	LC12010012	Choke Coil,	8Т
LT12	1	LC15000110	Choke Coil,	2T
T13	1	LC13320070	Choke Coil,	3.3µH
_T14	1	LC15000110	Choke Coil,	2T
T15	1	LA70260060	Antenna Coil	
_T17	1	LC12010012	Choke Coil,	8T
_T18	1	LC12010012	Choke Coil,	8T
_T19	1	LC12010012	Choke Coil,	8T
LT20	1	LC12010012		8T
XR01	1	XZ42094505	Crystal 20.94	5MHz

REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIPTION	REF. DESIG.	<b>Ο΄ΤΥ</b>	PART NO.	DESCRIPTION	
			P401-RX ANT. CAVITY	Q803	1	HD20001100	Diode 10D2	
			CIRCUIT BOARD					
P401	1	YH203C1330	P.W. Board, RX Ant. Cavity	J802	1	YJ01001390	Jack, EXT. Power	
LR01	1	LC11810030	Choke Coil, Cavity					
LR02	1	LC11810040	Choke Coil, Cavity	P804	1	YF200C0030	P804-LCD CIRCUIT BOARD P.W. Board, LCD	
			P801-REAR SWITCH	Q801	1	HC10006370	IC TP0401, LCD Driver	
			CIRCUIT BOARD	Q805	1	HQ20401440	Display Unit	
P801	1	YH203C1320	P.W. Board, Rear Switch	J807	1	YJ90000280	Jack, Connector	
R802	1	RA01040290	Trimming Resistor $100K\Omega(B)$	J808	1	YJ90000280	Jack, Connector	
11002		11401040250		J809	1	YJ10000520	Jack 9 pin	
S805	1	SK02010010	Push Switch, Reset					
S806	1	SS04030150	Slide Switch, Light/Bat. Check				P001-TONE CIRCUIT BOARD	
				P001	1	YF2030C0020	P.W. Board, Tone	
			P802-FRONT SWITCH				P001-CAPACITORS	
			CIRCUIT BOARD	C001	1	DK26103020	Ceramic $0.01\mu$ F 50V	
P802	1	YH203C2320	P.W. Board, Front Switch	C002	1	DK26104020	Ceramic $0.001\mu$ F 50V	
0000	1	0005104140		C003	1	EV33501660	Elect. 3.3µF 16V	
R803	1	GD05104140	Resistor 100K $\Omega$ ±5% ¼W	C004	1	DK26333010	Ceramic 0.033µF 50V	
S803	1	SS04030140	Slide Switch	C005	1	DK16471300	Ceramic 470pF ±10%	
S804	1	SS04030140	Slide Switch	C006	1	DK26473010	Ceramic $0.047\mu$ F 50V	
				C007 C008	1	DK16102300 DK16102300	Ceramic 0.001µF ±10% Ceramic 0.001µF ±10%	
				C008	1	EV47501660	Elect $4.7\mu$ F 16V	
			P803-EXT. POWER	0000	1 .	2 47 30 1000	4.741 100	
			CIRCUIT BOARD				P001-RESISTOR	
P803	1	YH203C2330	P.W. Board, EXT. Power	R001	1	GD05103180	10KΩ, 8/1W	
C803	1	DK16102300	Ceramic Cap. $0.001\mu$ F $\pm 10\%$	R002	1	GD05104180	100KΩ, 1/8W	
C803	1	DK16102300	Ceramic Cap. $0.001\mu$ F $\pm 10\%$ Ceramic Cap. $0.001\mu$ F $\pm 10\%$	R003	1	GD05105180	1MΩ, 1/8W	
0005		2.010102000		R004	1	GD05224180	220K Ω, 1/8W	

# 8. SPECIFICATIONS

1. General Specifications Frequency: ..... 430 ~ 440MHz F3 Type of emission: ..... Microphone input impedance: ..... 600Ω Speaker impedance: ..... 8Ω Operating supply voltage range: ..... 9.6 ~ 16V DC Normal power supply: External: ..... 13.8V DC Internal: ..... 1. UM-3 Ni-Cad battery x 10 2. UM-3 dry cell x 9 Dimensions: ... 129 (W) x 52 (H) x 190.5 (D) mm Weight: ..... 1.25kg (1.45kg including batteries) Power consumption: Reception standby: 25mA with battery saver ON Transmission: 600mA (at 1 watt ...... . 1

## ACCESSORIES

- Shoulder belt
   1
   External power plug
   1
- Schematic diagram
   1

DESCRIPTION	Trimming 1/8W ±5%, 1/8W ±5%, 1/8W Trimming	Trimming ±5% 1/8W ±5% 1/8W	<b>CONDUCTORS</b> 22C536(F) 1S1555 MC14093BCP 1S1555 2SC536(F) 2SC536(F) 2SC536(F) WZ090	ni n	
	10K.Ω, 820K.Ω, 47K.Ω, 39K.Ω, 6.8K.Ω, 10K.Ω,	10KΩ, 220KΩ, 10KΩ,	P001-SEMI Transistor Diode IC Diode Transistor Transistor Diode	Plug	Wîring
PART NO.	RA01030520 GD05824180 RA04730100 GD05633180 GD05632180 GD05682180 RA01030530	RA01030530 GD05224180 GD01503180	HT305360F0 HD20011050 HC10019170 HD20011050 HT305360F0 HT305360F0 HD30029090	YP10001060	Assembly and Wiring
α'τγ				-	
REF. DESIG.	R005 R006 R007 R008 R009 R010	R011 R012 R013	0001 0002 0003 0004 0005 0005 0005 0005	100r	(W01-99)

		into $50\Omega$ load)						
2.	Reception Specifications							
	Reception system:	Double superheterodyne						
	Intermediate:	1st IF: 21.4MHz						
		2nd IF: 455kHz						
	Sensitivity:	-5dB (20dB QS)						
		-7dB (12dB SINAD)						
	Pass bandwidth:	±7.5kHz						
	Selectivity:	Better than 60dB						
	Squelch sensitivity:	-14dB						
	AF output:	0.7 watt (into 8 ohms with 10% THD)						
	Load impedance:	8Ω						

- Transmission Specification

   Power output:
   1 watt
   Load impedance:
   50Ω
   Spurious attenuation:
   60dB
   Maximum frequency
   deviation:
   ±5kHz
   Modulation:
   Reactance
   modulation
   Audio frequency response:
   300 ~ 3000Hz
- \* These specifications are subject to change without notice in the event of improvements.

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Adjustment

(T01-99) (X01-00)

Correction